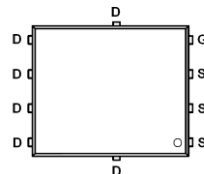
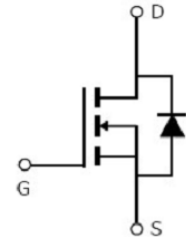


**Main Product Characteristics:**

$V_{DSS}$	30V
$R_{DS(on)}$	2.8m $\Omega$ (typ.)
$I_D$	100A


**PDFN5x6-8**

**Pin Assignments**

**Schematic Diagram**
**Features and Benefits:**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature


**Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

**Absolute Max Rating:**

Symbol	Parameter	Max.	Units
$I_D$ @ $T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS}$ @ 10V ①	100	A
$I_D$ @ $T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS}$ @ 10V ①	63	
$I_{DM}$	Pulsed Drain Current ②	400	
$P_D$ @ $T_C = 25^\circ\text{C}$	Power Dissipation ③	78	W
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$E_{AS}$	Single Pulse Avalanche Energy @ $L=0.5\text{mH}$	196	mJ
$T_J$ $T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

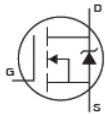
## Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-case ③	—	1.6	$^{\circ}\text{C}/\text{W}$

## Electrical Characterizes @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

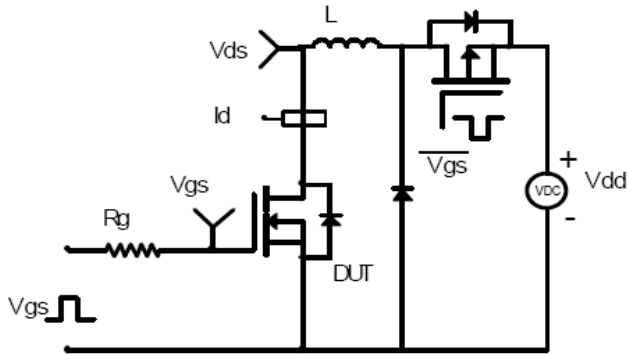
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	30	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	2.8	3.8	$\text{m}\Omega$	$V_{GS}=10\text{V}, I_D = 30\text{A}$
		—	4	5.2		$V_{GS}=4.5\text{V}, I_D = 20\text{A}$
$V_{GS(th)}$	Gate threshold voltage	1	—	2.2	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
$I_{DSS}$	Drain-to-Source leakage current	—	—	1	$\mu\text{A}$	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$
$I_{GSS}$	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 20\text{V}$
		—	—	-100		$V_{GS} = -20\text{V}$
$C_{iss}$	Input capacitance	—	2900	—	pF	$V_{GS} = 0\text{V}$
$C_{oss}$	Output capacitance	—	370	—		$V_{DS} = 15\text{V}$
$C_{rss}$	Reverse transfer capacitance	—	310	—		$f = 1\text{MHz}$
$Q_g$	Total gate charge	—	58	—	nC	$I_D = 30\text{A},$ $V_{DS}=15\text{V},$ $V_{GS} = 10\text{V}$
$Q_{gs}$	Gate-to-Source charge	—	12	—		
$Q_{gd}$	Gate-to-Drain("Miller") charge	—	13	—		
$t_{d(on)}$	Turn-on delay time	—	10	—	ns	$V_{GS}=10\text{V}, V_{DS} = 15\text{V},$ $R_{GEN}=3\Omega, I_D = 30\text{A}$
$t_r$	Rise time	—	30	—		
$t_{d(off)}$	Turn-Off delay time	—	45	—		
$t_f$	Fall time	—	18	—		

## Source-Drain Ratings and Characteristics

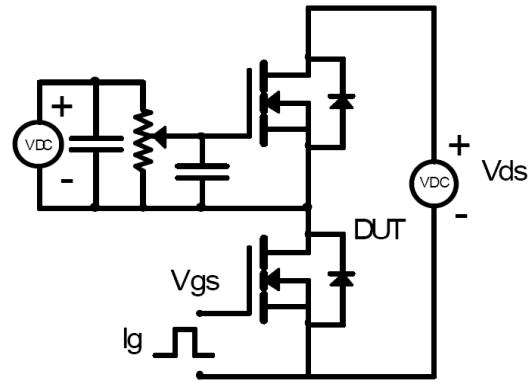
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	100	A	MOSFET symbol showing the integral reverse p-n junction diode. 
$I_{SM}$	Pulsed Source Current (Body Diode)	—	—	400	A	
$V_{SD}$	Diode Forward Voltage	—	—	1.2	V	$I_S=30\text{A}, V_{GS}=0\text{V}$
$t_{rr}$	Reverse Recovery Time	—	16	—	ns	$T_J = 25^{\circ}\text{C}, I_F = 30\text{A}, di/dt = 100\text{A}/\mu\text{s}$
$Q_{rr}$	Reverse Recovery Charge	—	7	—	nC	

## Test Circuits and Waveforms

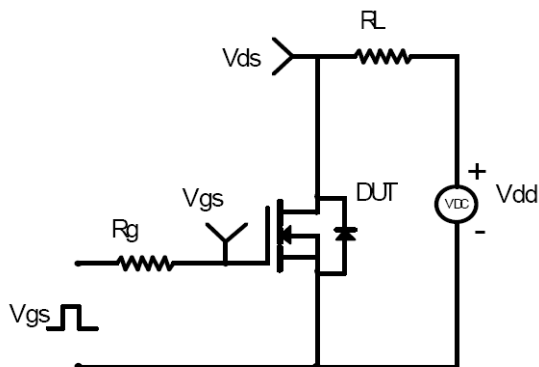
**EAS Test Circuit:**



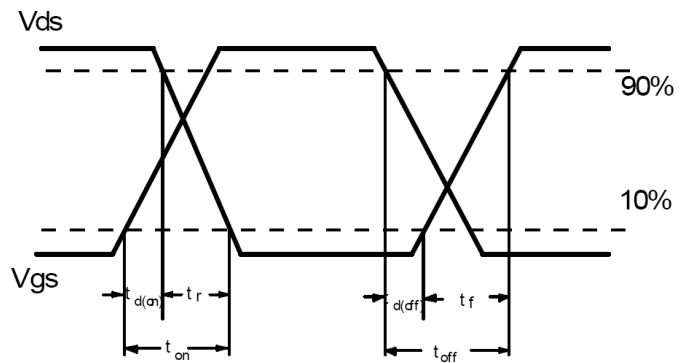
**Gate Charge Test Circuit:**



**Switching Time Test Circuit:**

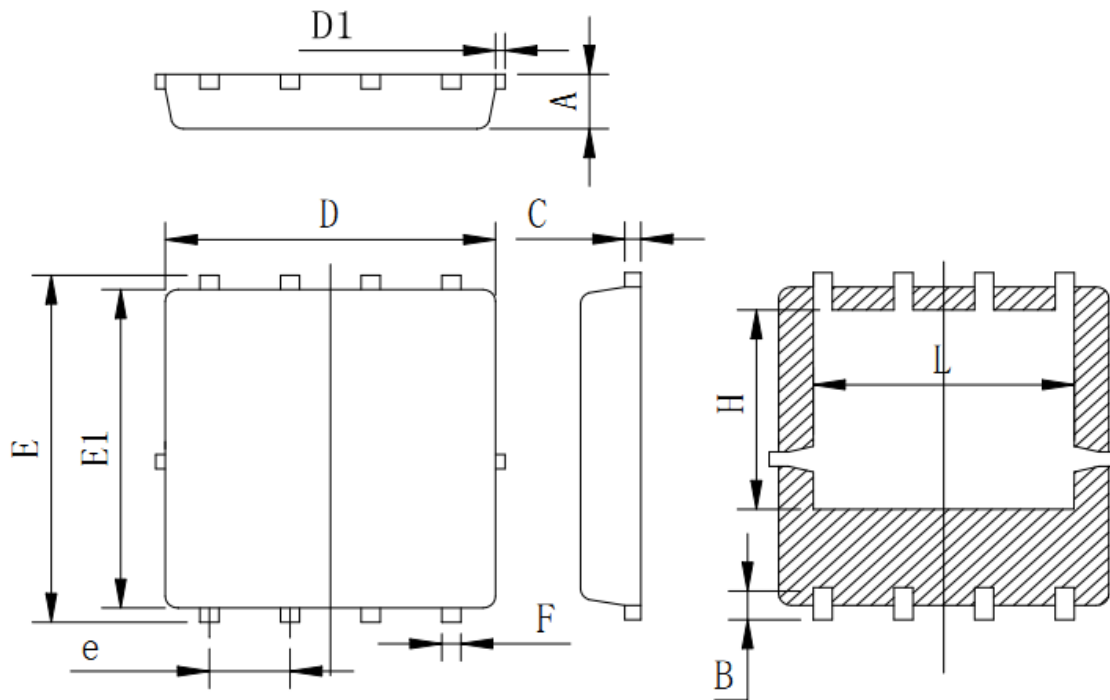


**Switching Waveforms:**



## Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation  $P_D$  is based on max. junction temperature, using junction-to-case thermal resistance.

**Mechanical Data:**


Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.48	0.58	0.68
C	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
E	5.90	6.05	6.20
E1	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
H	3.27	3.47	3.67
L	3.80	4.00	4.20

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