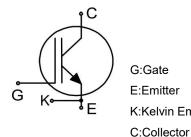


Main Product Characteristics:

Vces	1250 V
Ic	100 A
V _{CE(sat)}	1.75 V





G:Gate E:Emitter K:Kelvin Emitter

TO-247PLUS-4L

Schematic Diagram

Features and Benefits:

- Trench FS technology offering
- High speed switching
- Low gate charge and V_{CE(sat)}
- High ruggedness, temperature stable behavior
- Maximum junction temperature 175°C



Applications:

- Solar inverters
- Uninterruptible power supplies
- Motor drives
- Air condition

Absolute Max Rating:

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	1250	V
V _{GES}	Gate- Emitter Voltage	±30	V
Ic	Collector Current	200	
	Collector Current @T _C = 100 °C	100	
I _{Cpuls}	Pulsed Collector Current, t _p limited by T _{jmax}	400	
-	Turn off safe operating area,V _{CE} =1200V,T _J =175°C	400	A
lF	Diode Continuous Forward Current @Tc = 100 °C	100	
Іғм	Diode Maximum Forward Current	400	
P _D	Power Dissipation @ T _C = 25°C	750	W
	Power Dissipation @ T _C = 100°C	376	W
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +175	°C
TL	Maximum Temperature for Soldering	260	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
В	Thermal Resistance,Junction-to-case for IGBT	_	0.2	°C/W
R _{eJC}	Thermal Resistance,Junction-to-case for Diode	_	1.4	°C/W
R _{θJA}	Thermal Resistance,Junction-to-ambient	_	40	°C/W

Electrical Characteristics @T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V(BR)CES	Collector-Emitter Breakdown Voltage	1250	_	_	V	Vge=0V,Ice=1mA	
Va=: :: (Callandar Franklan Catanatian Valtana	_	1.75	2.2	V	Ic=100A ,VgE=15V @T _J =25°C	
VCE(sat)	Collector-Emitter Saturation Voltage	_	2.45	_	V	Ic=100A ,VgE=15V@T _J =175°C	
VGE(th)	Gate Threshold Voltage	4.5	4.8	6.5	V	Ic=250uA,VcE=VgE	
Ices	Collector-Emitter Leakage Current	_	_	10	μA	Vge =0V,Vce=1200V	
laa	Cata to Emitter Boyers Lookers	_	_	100	n 1	VgE=30V,VcE=0V	
Iges	Gate to Emitter Reverse Leakage	_	_	-100	nA	V _{GE} =-30V,V _{CE} =0V	
Cies	Input capacitance	_	12.18	_	nF	V _{GS} = 0V	
Coes	Output capacitance	_	166	_	ъг	V _{DS} = 100V	
Cres	Reverse transfer capacitance	_	108	_	pF	f = 1MHz	
$t_{\text{d(on)}} \\$	Turn-on delay time	_	86	_		V_{CC} =600V, V_{GE} =0.0/15.0V, R_{G} =10.0 Ω , I_{C} =90A	
t _r	Rise time		50	_	ns		
$t_{\text{d(off)}}$	Turn-Off delay time	_	850	_	113		
t _f	Fall time	_	52	_			
Eon	Turn-On Switching Loss	_	10.7	_		V _{CC} =600V,	
Eoff	Turn-Off Switching Loss	_	7.3	_	mJ	V_{GE} =0.0/15.0V, R_{G} =10.0 Ω ,	
Ets	Total Switching Loss	_	18	_		Ic=90A	
Qg	Total Gate Charge	_	669	_		\/000\/ I400A	
Qge	Gate to Emitter Charge	_	107	_	nC	Vcc=960V, Ic=100A,	
Qgc	Gate to Collector Charge	_	258	_		V _{GE} =15V	

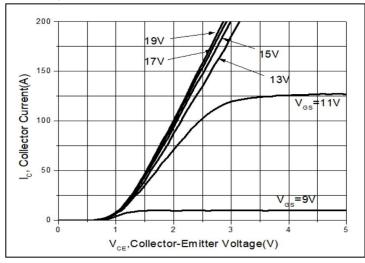
Electrical Characteristics of the Diode@T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
VFM	Diode Forward Voltage	_	2.3	4	V	I _F =100A,V _{GE} =0V
t _{rr}	Reverse Recovery Time	_	85	_	ns	T - 25°C -400A
Q _{rr}	Reverse Recovery Charge	_	2.2	_	μC	$T_J = 25^{\circ}C$, $I_F = 100A$, VR = 600V, VGE = 0.0/15.0V
IRRM	Diode Peak Reverse Recovery	_	48	_	Α	
	Current					





Typical Electrical and Thermal Characteristics



200

160

25° C

150° C

150° C

40

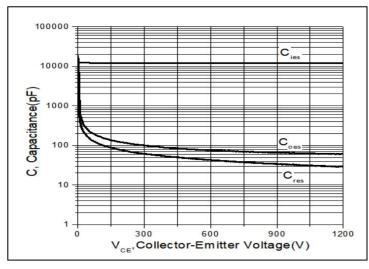
0

3

V_{GE}, Gate- Emitter Voltage (V)

Figure 1. Typical Output Characteristics

Figure 2. Typical Transfer Characteristics



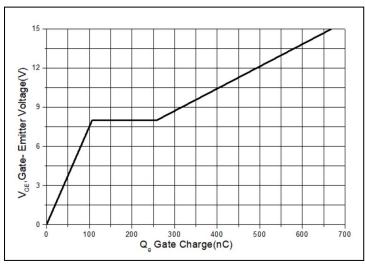


Figure 3. Typical Capacitance

800 640 150 160 160 175 150 175 175 175 175 175

Figure 4. Typical Gate Charge

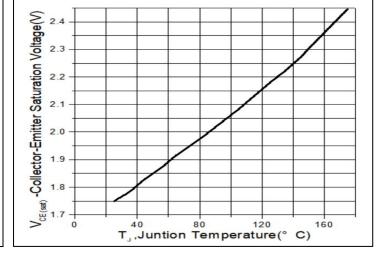


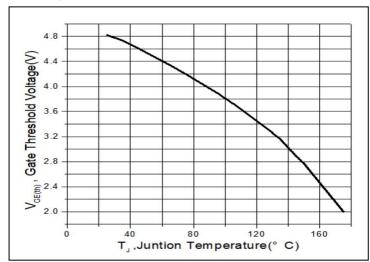
Figure 5. Power Dissipation vs. Case Temperature

Figure 6. Collector-Emitter Saturation Voltage vs. Temperature



SMG120NA0EF

Typical Electrical and Thermal Characteristics



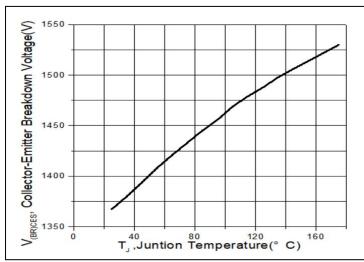


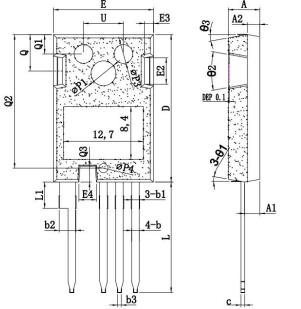
Figure 7. Gate Threshold Voltage vs. Temperature

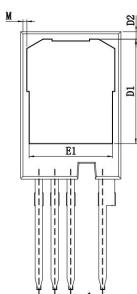
Figure 8. Collector-Emitter Breakdown Voltage vs. Temperature

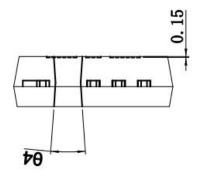




Mechanical Data:







SYMBOL	mm					
SIMBOL	MIN	NOM	MAX			
* A	4. 83	5. 02	5. 21			
* A1	2. 29	2. 42	2. 54			
A2	1. 91	2. 00	2. 16			
* b	1.07	1. 20	1. 33			
№ b1	1. 15	1. 30	1. 45			
№ b2	2. 39	2. 67	2. 94			
ъ3	0. 45	0. 60	0. 75			
* C	0. 55	0. 60	0. 68			
* D	23. 30	23. 45	23. 60			
D1	16. 35	16. 65	16. 95			
D2	0. 95	1. 19	1. 25			
₩ E	15. 75	15. 94	16. 13			
E1	13. 05	13. 25	13. 45			
E2	4.00	4. 40	4. 80			
E3	1.00	1. 45	1. 90			
E4	2. 40	2. 80	3. 20			
*e	2. 50	2. 54	2. 58			
*e1	5. 03	5. 08	5. 13			
₩L	17. 31	17. 57	17. 82			
* L1	-	-	4. 37			
M	0.40	0. 60	0. 80			
ФР1	3. 80	4. 00	4. 20			
ФРЗ	2. 80	3. 00	3. 20			
ФР4	1. 30	1. 50	1. 70			
Q	5. 49	5. 79	6. 00			
Q1	2. 80	3. 10	3. 40			
Q2	19. 95	21. 25	21. 55			
Q3	2. 35	2. 50	2. 65			
U	6. 05	6. 35	6. 55			
81	6°	10°	13°			
92	16°	20°	24°			
Ө3	6°	10°	13°			
84	5°	8°	11°			





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