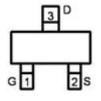
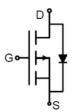


Main Product Characteristics:

V _{DSS}	-60V				
R _{DS} (on)	90mΩ(typ)				
I D	-5A				







SOT-23

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications

Absolute Max Rating:

Symbol	Max.	Units	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	-5	
I _D @ T _A = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	-3	Α
I _{DM}	Pulsed Drain Current ②	-20	
P _D @T _A = 25°C	Power Dissipation ③	1.5	W
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-to-Source Voltage	± 20	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{θJA}	Junction-to-Ambient (t \leq 10s) $\stackrel{\textcircled{4}}{=}$	_	81	°C/W

Electrical Characteristics @T」=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-60	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	90	120	mΩ	V _{GS} =-10V,I _D = -5A
		_	110	145		V _{GS} =-4.5V,I _D = -3A
V _{GS(th)}	Gate threshold voltage	-1	_	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	<u> </u>	-1	μA	V _{DS} =-60V, V _{GS} =0V
	Gate-to-Source forward leakage	_	_	100	^	V _{GS} =20V
I _{GSS}	Gate-to-Source reverse leakage	_	_	-100	- nA	V _{GS} = -20V
Qg	Total gate charge	_	6	_		I _D = -5A
Q _{gs}	Gate-to-Source charge	_	0.98	_	nC	V _{DD} =-30V
Q _{gd}	Gate-to-Drain("Miller") charge	_	0.7	_		V _{GS} = -10V
t _{d(on)}	Turn-on delay time	_	7	_		V _{GS} =-10V,
tr	Rise time	_	9	_	0	V _{DS} =-30V,
t _{d(off)}	Turn-Off delay time	_	17	_	- nS	R _{GEN} =5Ω
t _f	Fall time	_	5	_		I _D =-2A
C _{iss}	Input capacitance	_	307	_	pF	V _{GS} = 0V
Coss	Output capacitance	_	68	_		V _{DS} = -25V
Crss	Reverse transfer capacitance	_	8	_		f =1MHz

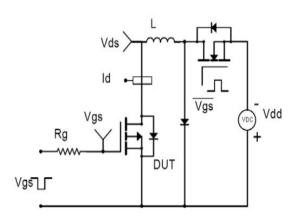
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
Is	Continuous Source Current (Body Diode) ①	_	_	-5	А	MOSFET symbol	
Іѕм	Pulsed Source Current (Body Diode)	_	_	-20	A	integral reverse	
V _{SD}	Diode Forward Voltage	_	_	-1.2	V	I _S =-3A, V _{GS} =0V,T _J = 25°C	
trr	Reverse Recovery Time	_	25	_	ns	T _J = 25°C, I _F =-4A,	
Qrr	Reverse Recovery Charge	_	31	_	nC	di/dt = 100A/µs	

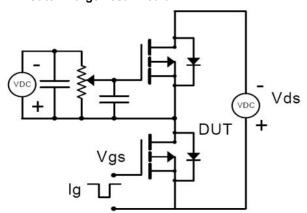


Test Circuits and Waveforms:

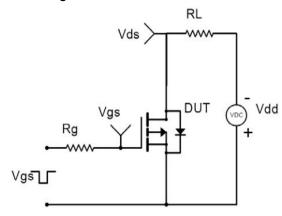
EAS Test Circuit:



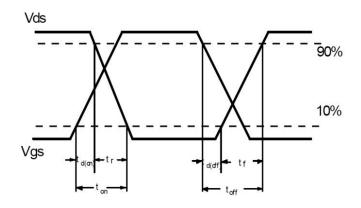
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



Version : Preliminary

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- $\ \ \,$ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C



Mechanical Data:

Symbol	Dimension I	n Millimeters	Dimension In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.95TYP 0.037TYF		7TYP		
e1	1.800	2.000	0.071	0.079	
L	0.55REF		0.022REF		
L1	0.300	0.500	0.012	0.020	
θ	00	8 ⁰	00	8 ⁰	





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