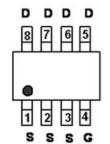


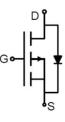
SMT004P40H1

Main Product Characteristics:

V _{DSS}	-40V				
R _{DS} (on)	46mΩ (typ.)				
Ι _D	-4.2A				







SOP-8

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units	
I _D @ T _A = 25°C	Continuous Drain Current ①	-4.2		
I _D @ T _A = 100°C	Continuous Drain Current ①	-2.7	A	
I _{DM}	Pulsed Drain Current 2	-16.8		
P _D @T _A = 25°C	Power Dissipation ③	1.83	W	
V _{DS}	Drain-Source Voltage	-40	V	
V _{GS}	Gate-to-Source Voltage	± 20	V	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{0JA}	Junction-to-ambient (t \leq 10s) ④		68	°C/W

Electrical Characteristics @T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-40	_	_	V	V _{GS} = 0V, I _D = -250µA
D		—	46	60	0	V _{GS} =-10V,I _D = -4A
$R_{DS(on)}$	Static Drain-to-Source on-resistance		59	78	mΩ	V _{GS} =-4.5V,I _D = -3A
$V_{\text{GS(th)}}$	Gate threshold voltage	-1	_	-2.5	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
I _{DSS}	Drain-to-Source leakage current		_	-1	μA	$V_{DS} = -40V, V_{GS} = 0V$
	Cata to Source forward lookage		_	100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source forward leakage		_	-100		V _{GS} = -20V
Qg	Total gate charge		12	_		I _D = -4A,
Q _{gs}	Gate-to-Source charge		3.5	_	nC	V _{DS} =-20V,
Q _{gd}	Gate-to-Drain("Miller") charge		2.8	_		V _{GS} = -10V
t _{d(on)}	Turn-on delay time		7.6	_		
tr	Rise time		3.6	_]	V _{GS} =-10V, V _{DS} =-20V,
t _{d(off)}	Turn-Off delay time		19	_	ns 	$R_{GEN}=3\Omega, R_{L}=5\Omega$
t _f	Fall time		4.6	_		
Ciss	Input capacitance	_	905	—		V _{GS} = 0V
Coss	Output capacitance	_	60	_	pF	V _{DS} =-20V
C _{rss}	Reverse transfer capacitance	_	46	_		f = 1MHz

Source-Drain Ratings and Characteristics

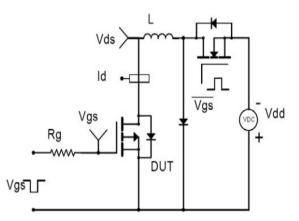
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current	Current4.2		4.0	^	MOSFET symbolî
Is	(Body Diode) ①		-4.2	-4.2 A	showing the	
I _{SM}	Pulsed Source Current	_	_	-16.8	А	integral reverse G⊶ 🕂 🛊
	(Body Diode) ①					p-n junction diode r_{s}
V _{SD}	Diode Forward Voltage	_	—	-1.2	V	I _S =-4A, V _{GS} =0V



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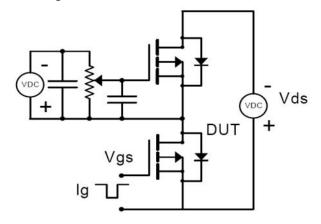
Test Circuits and Waveforms

EAS Test Circuit:

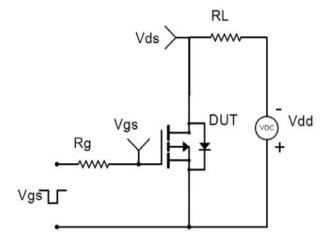


Gate Charge Test Circuit:

Switching Waveforms:



Switching Time Test Circuit:



Notes:

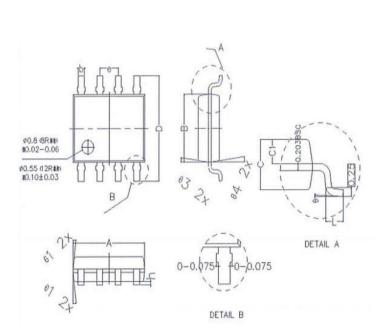
- ①Calculated continuous current based on maximum allowable junction temperature.
- 2 Repetitive rating; pulse width limited by max. junction temperature.
- $\ensuremath{\textcircled{3}}$ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.
- (4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}C$





Mechanical Data:

SOP-8 Package Outline (Unit:mm)



	COMMON (UNITS OF			eun)		
	MIN	NO	RMAL	MAX		
Α	4.800	4.9	900	5.000		
В	3.800	3.9	900	4.000		
С	1.350	1.4	450	1.550		
C1	0.650	0.	700	0.750		
D	5.950	6.	120	6.280		
L	0.500	0.6	500	0.700		
b	0.350	0.4	400	0.450		
h	0.070	0.1	50	0.250		
е	1.270TYPE					
θ1	7º TYPE(8R)	12' 1	TYPE(12R)		
θ2	7' TYPE(8R) 10' TYPE(12R)					
θз	8' TYPE(8R) 12' TYPE(12R			TYPE(12R)		
θ4	8' TYPE(8R) 10' TYPE(12R)					
θ	0* ~ 8*					



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