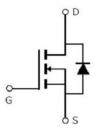


#### **Main Product Characteristics:**

V <sub>DSS</sub>	40V
R <sub>DS</sub> (on)	11.5mΩ (typ.)
I <sub>D</sub>	36A





TO-252

Schematic Diagram

#### **Features and Benefits:**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



### **Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

## **Absolute Max Rating:**

Symbol	Parameter	Max.	Units
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V ①	36	
I <sub>D</sub> @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V ①	25	Α
I <sub>DM</sub>	Pulsed Drain Current ②	144	
P <sub>D</sub> @T <sub>C</sub> = 25°C	Power Dissipation ③	31	W
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C



### **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	4	°C/W

### **Electrical Characteristics** @T<sub>A</sub>=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
В	Static Drain-to-Source on-resistance	_	11.5	15	m0	V <sub>GS</sub> =5V,I <sub>D</sub> = 20A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	15.5	20	mΩ	V <sub>GS</sub> =4.5V,I <sub>D</sub> = 15A
V <sub>GS(th)</sub>	Gate threshold voltage	1	_	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I <sub>DSS</sub>	Drain-to-Source leakage current	_	_	1	μA	V <sub>DS</sub> = 40V,V <sub>GS</sub> = 0V
	Cata to Source forward looked	_	_	100	n 1	V <sub>GS</sub> =20V
I <sub>GSS</sub>	Gate-to-Source forward leakage	_	_	-100	nA	V <sub>GS</sub> = -20V
C <sub>iss</sub>	Input capacitance	_	1314	_		V <sub>GS</sub> = 0V
Coss	Output capacitance	_	95	_	pF	V <sub>DS</sub> = 20V
Crss	Reverse transfer capacitance	_	79	_		f = 1MHz
Qg	Total gate charge	_	25	_		I <sub>D</sub> = 20A,
Q <sub>gs</sub>	Gate-to-Source charge	_	4	_	nC	V <sub>DS</sub> =20V,
Q <sub>gd</sub>	Gate-to-Drain("Miller") charge	_	6	_		V <sub>GS</sub> = 10V
t <sub>d(on)</sub>	Turn-on delay time	_	13	_		
t <sub>r</sub>	Rise time	_	4	_		V <sub>GS</sub> =10V, V <sub>DS</sub> =20V,
t <sub>d(off)</sub>	Turn-Off delay time	_	31	_	ns	$R_{GEN}=6\Omega,R_L=1\Omega$
t <sub>f</sub>	Fall time	_	3	_		

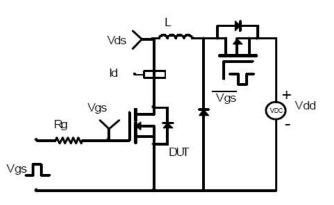
# **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			20	^	MOSFET symbol
I <sub>S</sub>	(Body Diode)	_	_	36	A	showing the
	Pulsed Source Current			444	^	integral reverse
Ism	(Body Diode)	_	_	144	A	p-n junction diode.
V <sub>SD</sub>	Diode Forward Voltage	_	_	1.2	V	I <sub>S</sub> =20A, V <sub>GS</sub> =0V
t <sub>rr</sub>	Reverse Recovery Time	_	18	_	ns	$T_J = 25^{\circ}C$ , $I_F = 20A$ , $di/dt =$
Q <sub>rr</sub>	Reverse Recovery Charge	_	11	_	nC	100A/µs

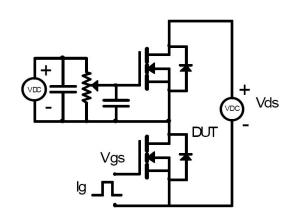


### **Test Circuits and Waveforms**

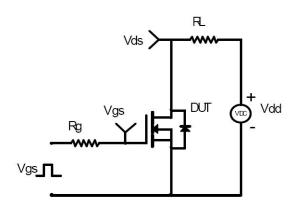
#### **EAS Test Circuit:**



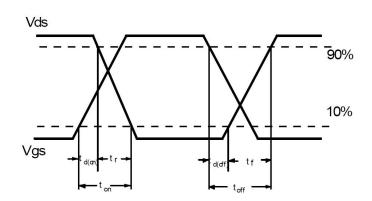
#### **Gate Charge Test Circuit:**



#### **Switching Time Test Circuit:**



#### **Switching Waveforms:**

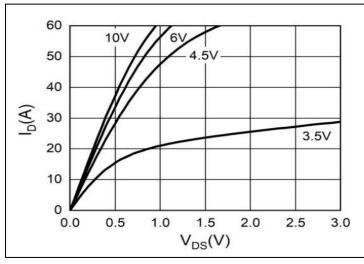


#### Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



# **Typical Electrical and Thermal Characteristics**



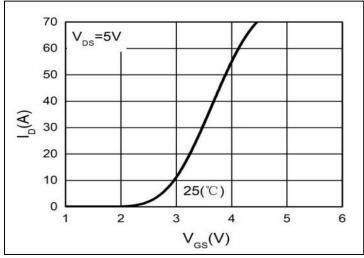
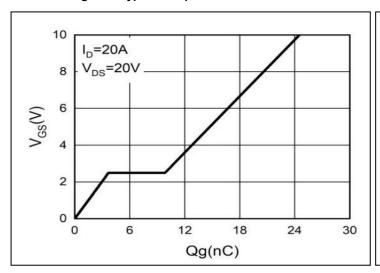


Figure 1. Typical Output Characteristics

Figure 2. Transfer Characteristics



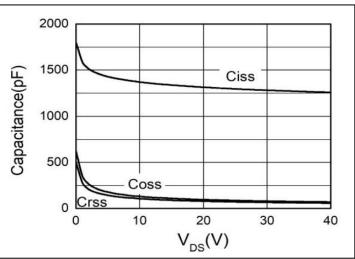
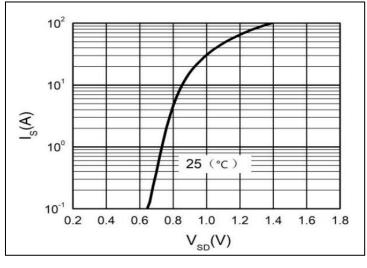


Figure 3. Gate Charge Waveforms

Figure 4. Capacitance



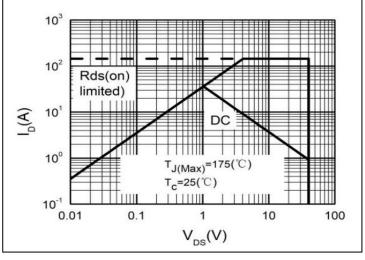
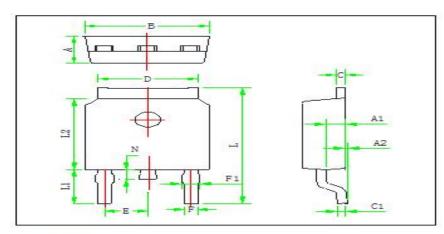


Figure 5. Body-Diode Characteristics

Figure 6. Maximum Safe Operating Area



## **Mechanical Data:**



Symbol	Min	Typ	Max		
A	2.20	2.30	2.40		
A1	0.91	1.01	1.11		
A2	0.05	0.15	0.25		
В	6.45	6.60	6.75		
C	0.45	0.50	0.58		
C1	0.45	0.50	0.58		
D	5.12	5.32	5.52		
E	2.286 TYP				
F	0.66 0.76 0.8				
F1	0.66	0.86	1.06		
L	9.60	9.90	10.20		
L1	2.6	2.8	3.0		
L2	5.95	6.10	6.25		
N	0.60	0.80	1.00		





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