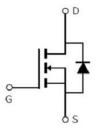


Main Product Characteristics:

V _{DSS}	40V
R _{DS} (on)	6.7mΩ (typ.)
I _D	57A





TO-252

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	57	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	40	Α
I _{DM}	Pulsed Drain Current ②	228	
P _D @T _C = 25°C	Power Dissipation ③	45	W
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-to-Source Voltage	± 20	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	2.8	°C/W

Electrical Characteristics @T_A=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	40	_	_	V	V _{GS} = 0V, I _D = 250μA
Б	Static Ducin to Course on marietane	_	6.7	8.7	0	V _{GS} =5V,I _D = 20A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	7.9	10.3	mΩ	V _{GS} =4.5V,I _D = 15A
V _{GS(th)}	Gate threshold voltage	1	_	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	1	μA	V _{DS} = 40V,V _{GS} = 0V
	Cata to Source forward lookage	_	_	100	n A	V _{GS} =20V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -20V
C _{iss}	Input capacitance	_	2090	_		V _{GS} = 0V
Coss	Output capacitance	_	160	_	pF	V _{DS} = 20V
Crss	Reverse transfer capacitance	_	130	_		f = 1MHz
Qg	Total gate charge	_	41	_		I _D = 20A,
Qgs	Gate-to-Source charge	_	6	_	nC	V _{DS} =20V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	8	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	8.4	_		
t _r	Rise time	_	6.2	_		V _{GS} =10V, V _{DS} =20V,
t _{d(off)}	Turn-Off delay time	_	40	_	ns	$R_{GEN}=3\Omega, R_L=1\Omega$
t _f	Fall time	_	7.8	_		

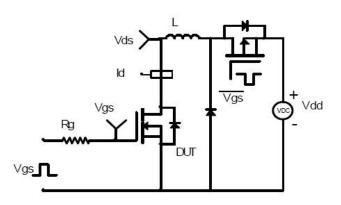
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			E7	_	MOSFET symbol
l _S	(Body Diode)	_	_	57	A	showing the
	Pulsed Source Current			220	^	integral reverse
Isм	(Body Diode)	_	_	228	A	p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =20A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	18	_	ns	$T_J = 25^{\circ}C, I_F = 20A, di/dt =$
Q _{rr}	Reverse Recovery Charge	_	13	_	nC	100A/µs

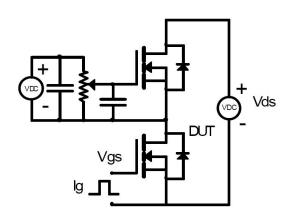


Test Circuits and Waveforms

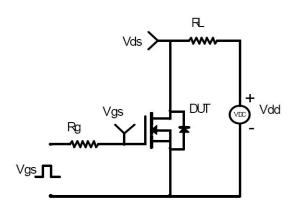
EAS Test Circuit:



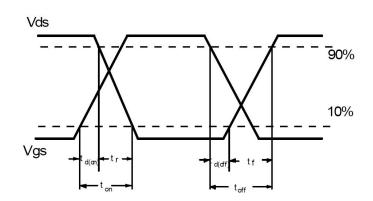
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



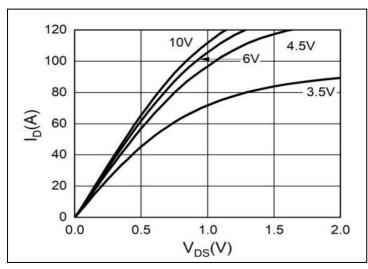
Version: Preliminary

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



Typical Electrical and Thermal Characteristics



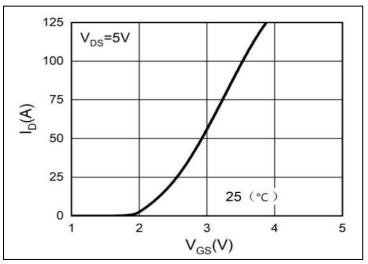
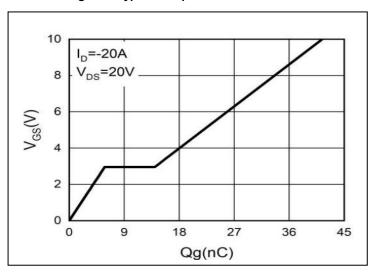


Figure 1. Typical Output Characteristics

Figure 2. Transfer Characteristics



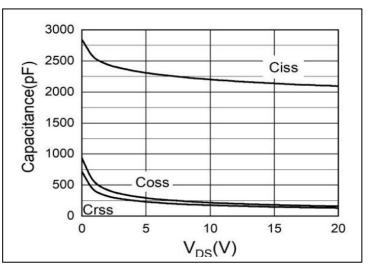
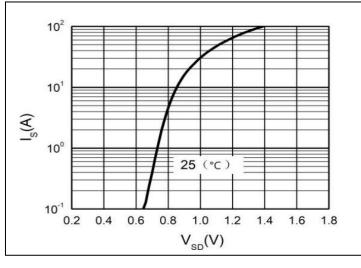


Figure 3. Gate Charge Waveforms

Figure 4. Capacitance



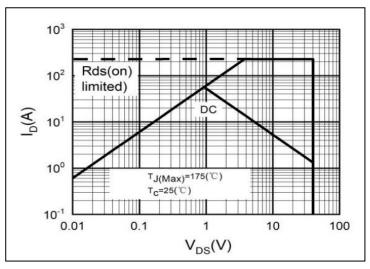
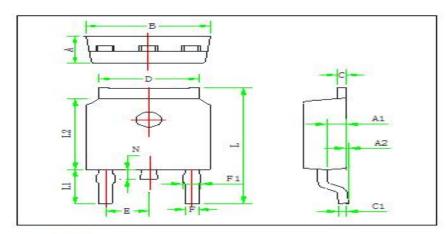


Figure 5. Body-Diode Characteristics

Figure 6. Maximum Safe Operating Area



Mechanical Data:



Symbol	Min	Typ	Max		
A	2.20	2.30	2.40		
A1	0.91	1.01	1.11		
A2	0.05	0.15	0.25		
В	6.45	6.60	6.75		
C	0.45	0.50	0.58		
C1	0.45	0.50	0.58		
D	5.12	5.32	5.52		
E	2.286 TYP				
F	0.66 0.76 0.8				
F1	0.66	0.86	1.06		
L	9.60	9.90	10.20		
L1	2.6	2.8	3.0		
L2	5.95	6.10	6.25		
N	0.60	0.80	1.00		





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