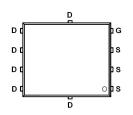
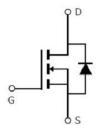


Main Product Characteristics:

V _{DSS}	40V			
R _{DS} (on)	4.5mΩ (typ.)			
I _D	57A			







PDFN5x6-8L

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	57	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	36	Α
I _{DM}	Pulsed Drain Current ②	228	
P _D @T _C = 25°C	Power Dissipation ③	33	W
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-to-Source Voltage	± 20	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	3.7	°C/W

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
D 01.1	Otatia Dania ta Carresa ara ara i t	_	4.5	5.9	0	V _{GS} =10V,I _D = 20A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	8	10.6	mΩ	V _{GS} =4.5V,I _D = 20A
V _{GS(th)}	Gate threshold voltage	1	_	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	1	μA	V _{DS} = 40V,V _{GS} = 0V
	Cata ta Causaa famusand la alcana	_	_	100	^	V _{GS} =20V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -20V
C _{iss}	Input capacitance	_	3000	_		V _{GS} = 0V
Coss	Output capacitance	_	250	_	pF	V _{DS} = 20V
Crss	Reverse transfer capacitance	_	170	_		f = 1MHz
Qg	Total gate charge	_	55	_		I _D = 20A,
Qgs	Gate-to-Source charge	_	9	_	nC	V _{DS} =20V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	14	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	15	_		
t _r	Rise time	_	8	_		V _{GS} =10V, V _{DS} =20V,
t _{d(off)}	Turn-Off delay time	_	44	_	ns	$R_{GEN}=3\Omega, R_L=1\Omega$
t _f	Fall time	_	15	_		

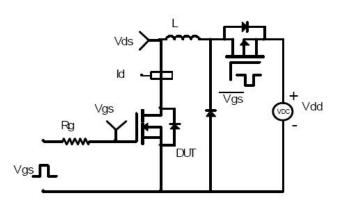
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
1	Continuous Source Current			57	^	MOSFET symbol
Is	(Body Diode)	_	_	57	A	showing the
	Pulsed Source Current			220	^	integral reverse
Іѕм	(Body Diode)	_ _ _ 228	228	28 A	p-n junction diode.	
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =20A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	44	_	ns	$T_J = 25^{\circ}C$, $I_F = 20A$, $di/dt =$
Qrr	Reverse Recovery Charge	_	49	_	nC	100A/µs

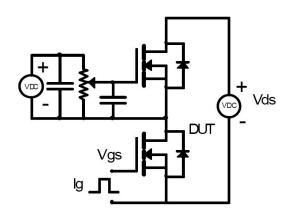


Test Circuits and Waveforms

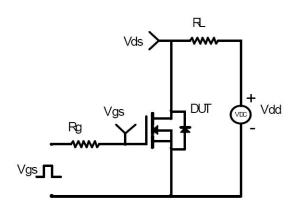
EAS Test Circuit:



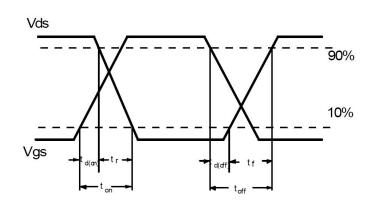
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



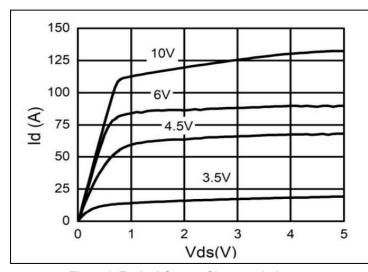
Version: Preliminary

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- $\ \ \,$ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.



Typical Electrical and Thermal Characteristics



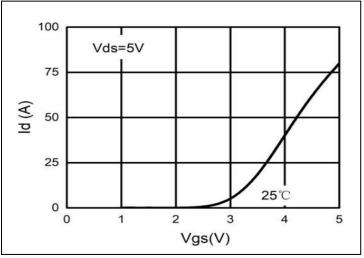
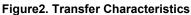
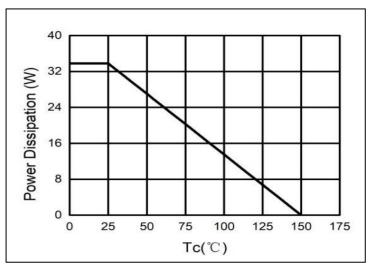


Figure 1. Typical Output Characteristics





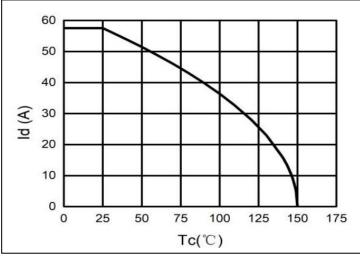
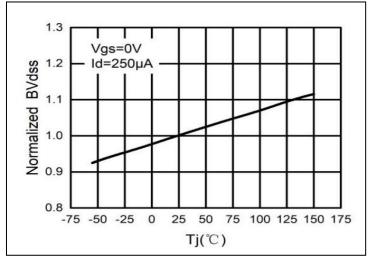


Figure 3. Power Dissipation

Figure 4. Drain Current



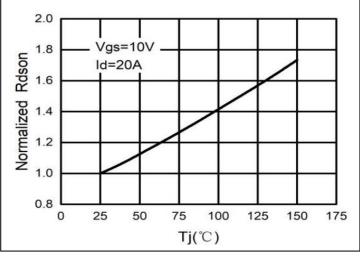


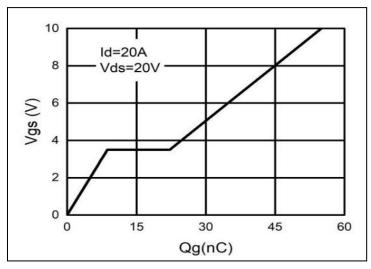
Figure 5. BV_{DSS} vs Junction Temperature

Figure 6. R_{DS(ON)} vs Junction Temperature





Typical Electrical and Thermal Characteristics



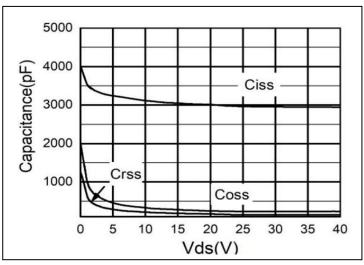
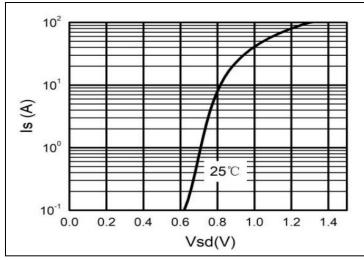


Figure 7. Gate Charge Waveforms

Figure8. Capacitance



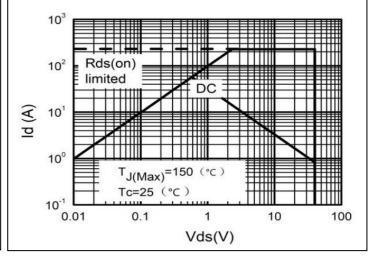
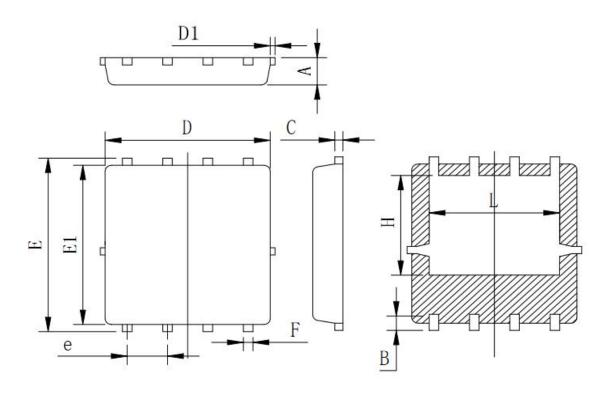


Figure 9. Body-Diode Characteristics

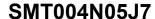
Figure 10. Maximum Safe Operating Area



Mechanical Data:



Symbol	Min	Тур	Max
A	0.90	0.95	1.00
В	0.48	0.58	0.68
С	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
Е	5.90	6.05	6.20
El	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
Н	3.27	3.47	3.67
L	3.80	4.00	4.20





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