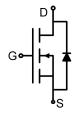


SMT002NA4G1

Main Product Characteristics:

V _{DSS}	20V				
R _{DS} (on)	131mΩ(typ.)				
Ι _D	1A				





SOT-23

Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Max.	Units	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V①	1	
I _D @ T _A = 100°C	Continuous Drain Current, V _{GS} @ 10V①	0.7	A
I _{DM}	Pulsed Drain Current2	4]
P _D @T _A = 25°C	Power Dissipation3	0.23	W
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-to-Source Voltage	± 10	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{0JA}	Junction-to-Ambient	_	543	°C /W

Electrical Characteristics @T_=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	20		_	V	$V_{GS} = 0V, I_D = 250 \mu A$
$R_{\text{DS(on)}}$	Static Drain-to-Source on-resistance		131	170	mΩ	V _{GS} =4.5V,I _D =0.5A
		_	190	247	mΩ	V _{GS} =2.5V,I _D =0.3A
$V_{\text{GS(th)}}$	Gate threshold voltage	0.4	—	1	V	V_{DS} = V_{GS} , I_D =250 μ A
I _{DSS}	Drain-to-Source leakage current	_	—	1	μA	V_{DS} =20V, V_{GS} = 0V
Igss	Gate-to-Source forward leakage	_	_	±10	μA	V _{GS} =±10V,V _{DS} =0V
Qg	Total gate charge	_	1.1	—		I _D = 0.9A,
Q_{gs}	Gate-to-Source charge	_	0.27	—	nC	V _{DS} =10V,
Q_{gd}	Gate-to-Drain("Miller") charge	_	0.21	—		V _{GS} = 4.5V
t _{d(on)}	Turn-on delay time	_	2	—		
t _r	Rise time		20	_		V _{GS} =4.5V, V _{DS} =10V,
$t_{d(off)}$	Turn-Off delay time		11		ns	R _{GEN} =10Ω I _D = 0.5A
t _f	Fall time	_	24	_		10 - 0.04
Ciss	Input capacitance	_	61	_		V _{GS} = 0V
Coss	Output capacitance		21	—	рF	V _{DS} = 10V
C _{rss}	Reverse transfer capacitance		13	_]	f = 1MHz

Source-Drain Ratings and Characteristics

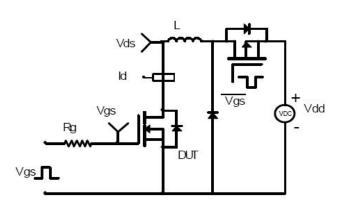
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
Is	Continuous Source Current	—	—	1	A	MOSFET symbol ந	
	(Body Diode)					showing the G⊶ H I	
Іѕм	Pulsed Source Current	_	_	4	А	integral reverse	
	(Body Diode)					p-n junction diode.	
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =0.5A, V _{GS} =0V	



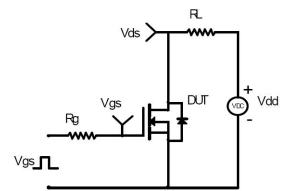
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Test Circuits and Waveforms

EAS Test Circuit:

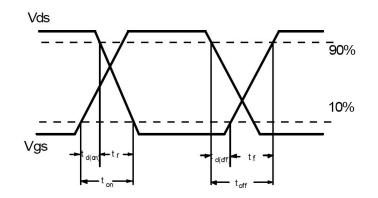


Switching Time Test Circuit:



Switching Waveforms:

Gate Charge Test Circuit:



Notes:

①Calculated continuous current based on maximum allowable junction temperature.

②Repetitive rating; pulse width limited by max. junction temperature.

3 The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.

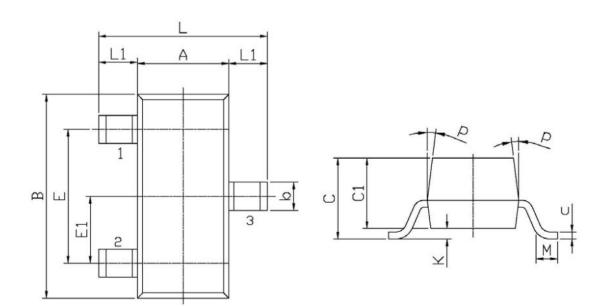
(4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25 °C.



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Mechanical Data:

SOT-23 Package Outline(Unit:mm)



	Dimensions in Millimeter			Dimensions in		
Symbol			Symbol	Millimeter		
	Min	Max		Min	Max	
L	2.2	2.7	С	1.30 Max		
L1	0.45	0.65	C1	0.90	1.20	
A	1.15	1.50	С	0.05	0.20	
В	2.70	3.10	K	0	0.10	
E	1.70	2.10	М	0.20 Min		
E1	0.85	1.05	Р	7°		
b	0.35	0.55				





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