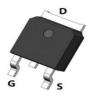
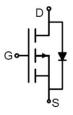


Main Product Characteristics:

V _{DSS}	-15V
R _{DS} (on)	12.5mΩ (typ.)
I _D	-18A





TO-252

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	-18	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	-12	Α
Ідм	Pulsed Drain Current ②	-72	
P _D @T _C = 25°C	Power Dissipation ③	11	W
V _{DS}	Drain-Source Voltage	-15	V
V _{GS}	Gate-to-Source Voltage	± 12	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
Reja	Thermal Resistance,Junction-to-ambient ④	_	11	°C/W

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-15	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
В	Static Drain-to-Source on-resistance	_	12.5	16.3	m0	V _{GS} = -4.5V,I _D = -10A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	17	22	mΩ	V _{GS} = -2.5V,I _D = -8A
V _{GS(th)}	Gate threshold voltage	-0.4	_	-1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	-1	μA	V _{DS} = -15V,V _{GS} = 0V
	Cata to Source forward looked	_	_	100	n 1	V _{GS} =12V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -12V
C _{iss}	Input capacitance	_	1370	_		V _{GS} = 0V
Coss	Output capacitance	_	235	_	pF	V _{DS} = -10V
Crss	Reverse transfer capacitance	_	215	_		f = 1MHz
Qg	Total gate charge	_	15	_		I _D = -3A,
Qgs	Gate-to-Source charge	_	2.2	_	nC	V _{DS} = -10V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	4.4	_		V _{GS} = -4.5V
t _{d(on)}	Turn-on delay time	_	10	_		
t _r	Rise time	_	30	_		V_{GS} = -4.5V, V_{DS} = -10V,
t _{d(off)}	Turn-Off delay time	_	28	_	ns	$R_{GEN}=2.5\Omega, I_D=-7A$
t _f	Fall time	_	8	_		

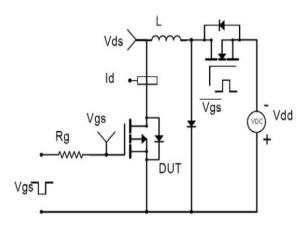
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			-18	^	MOSFET symbol □ i
Is	(Body Diode)	_	_	-10	A	showing the
	Pulsed Source Current			70	^	integral reverse
Ism	(Body Diode)	_	_	-72	A	p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	-1.2	V	I _S =-10A, V _{GS} =0V

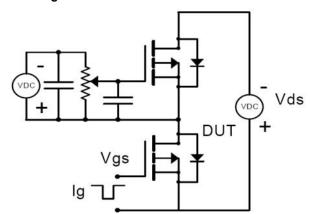


Test Circuits and Waveforms

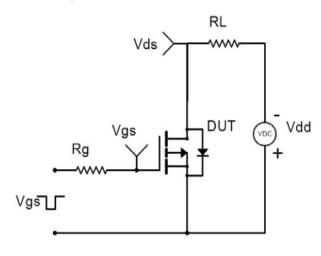
EAS Test Circuit:



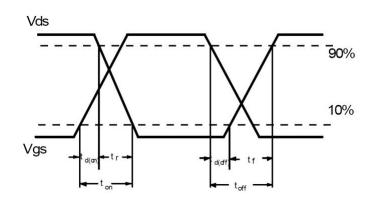
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



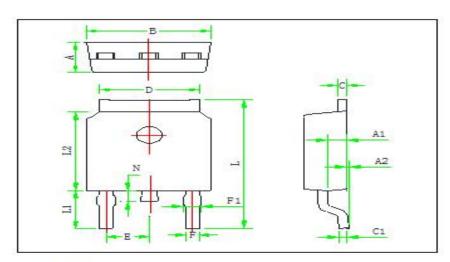
Version: Preliminary

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4 The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.



Mechanical Data:



Symbol	Min	Typ	Max	
A	2.20	2.30	2.40	
A1	0.91	1.01	1.11	
A2	0.05	0.15	0.25	
В	6.45	6.60	6.75	
C	0.45	0.50	0.58	
C1	0.45	0.50	0.58	
D	5.12	5.32	5.52	
E	2.286 TYP			
F	0.66	0.76	0.86	
F1	0.66	0.86	1.06	
L	9.60	9.90	10.20	
L1	2.6	2.8	3.0	
L2	5.95	6.10	6.25	
N	0.60	0.80	1.00	





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