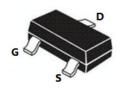
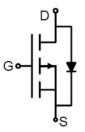


## **Main Product Characteristics:**

V <sub>DSS</sub>	-12V
R <sub>DS</sub> (on)	13.3mΩ (typ.)
I <sub>D</sub>	-9A





SOT23-3L

Schematic Diagram

### **Features and Benefits:**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



## **Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

# **Absolute Max Rating:**

Symbol	Parameter	Max.	Units
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V ①	-9	
I <sub>D</sub> @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V ①	-5.8	Α
I <sub>DM</sub>	Pulsed Drain Current ②	-36	
P <sub>D</sub> @T <sub>C</sub> = 25°C	Power Dissipation ③	2	W
V <sub>DS</sub>	Drain-Source Voltage	-12	V
V <sub>G</sub> S	Gate-to-Source Voltage	± 12	V
Eas	Single Pulse Avalanche Energy @ L=0.5mH	20	mJ
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C



## **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
Reja	Junction-to-ambient (t $\leq 10s$ ) $\textcircled{4}$	_	63	°C/W

## **Electrical Characterizes** @T<sub>A</sub>=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	-12	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Б	0	_	13.3	18	0	V <sub>GS</sub> = -4.5V,I <sub>D</sub> = -5A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	18.4	25	mΩ	V <sub>GS</sub> = -2.5V,I <sub>D</sub> = -4A
V <sub>GS(th)</sub>	Gate threshold voltage	-0.5	_	-1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
I <sub>DSS</sub>	Drain-to-Source leakage current	_	_	-1	μA	V <sub>DS</sub> = -12V,V <sub>GS</sub> = 0V
	Cata ta Sauraa famuard la akaga	_	_	100	n A	V <sub>GS</sub> =12V
I <sub>GSS</sub>	Gate-to-Source forward leakage	_	_	-100	nA	V <sub>GS</sub> = -12V
C <sub>iss</sub>	Input capacitance	_	1470	_		V <sub>GS</sub> = 0V
Coss	Output capacitance	_	312	_	pF	V <sub>DS</sub> = -25V
Crss	Reverse transfer capacitance	_	290	_		f = 1MHz
Qg	Total gate charge	_	20	_		I <sub>D</sub> = -5A,
Qgs	Gate-to-Source charge	_	5	_	nC	V <sub>DS</sub> = -6V,
Q <sub>gd</sub>	Gate-to-Drain("Miller") charge	_	6	_		V <sub>GS</sub> = -10V
t <sub>d(on)</sub>	Turn-on delay time	_	14.4	_		
t <sub>r</sub>	Rise time	_	5.5	_		$V_{GS}$ = -10V, $V_{DS}$ = -6V,
t <sub>d(off)</sub>	Turn-Off delay time	_	59.4	_	ns	$R_{GEN}=3\Omega, R_L=2\Omega$
t <sub>f</sub>	Fall time	_	21.6	_		

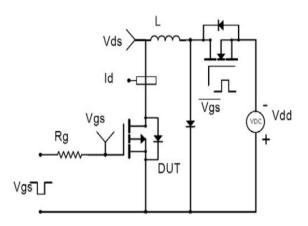
# **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			0	А	MOSFET symbol □ ்_
l <sub>S</sub>	(Body Diode)	_	_	-9		showing the
	Pulsed Source Current			00		integral reverse
Isм	(Body Diode)	_	_	-36	A	p-n junction diode.
V <sub>SD</sub>	Diode Forward Voltage	_	_	-1.2	V	I <sub>S</sub> =-10A, V <sub>GS</sub> =0V
t <sub>rr</sub>	Reverse Recovery Time	_	31.2	_	ns	$T_J = 25^{\circ}C$ , $I_F = -10A$ , $di/dt =$
Q <sub>rr</sub>	Reverse Recovery Charge	_	10.9	_	nC	100A/µs

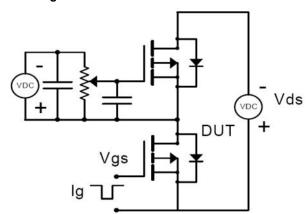


## **Test Circuits and Waveforms**

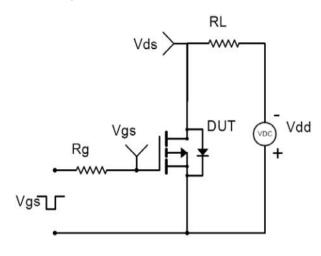
#### **EAS Test Circuit:**



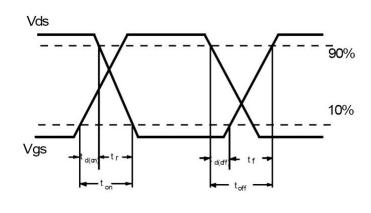
#### **Gate Charge Test Circuit:**



### **Switching Time Test Circuit:**



#### **Switching Waveforms:**



**Version: Preliminary** 

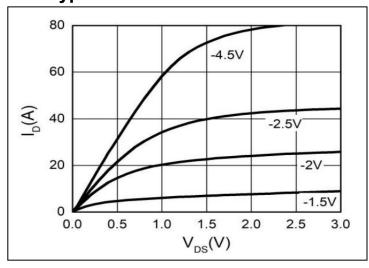
#### Notes:

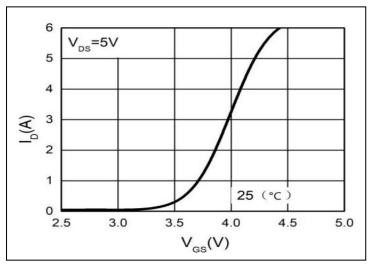
- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of  $R_{\texttt{9JA}}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C





# **Typical Electrical and Thermal Characteristics**





**Figure 1. Typical Output Characteristics** 

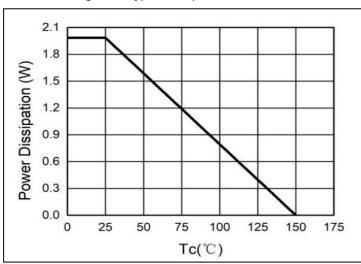


Figure 2. Transfer Characteristics

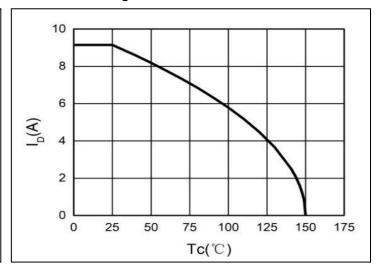


Figure 3. Power Dissipation

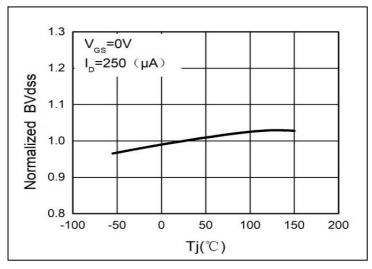


Figure 5. BV<sub>DSS</sub> vs Junction Temperature

Figure 4. Drain Current

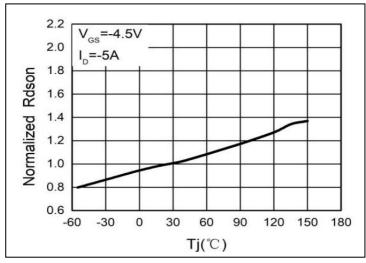
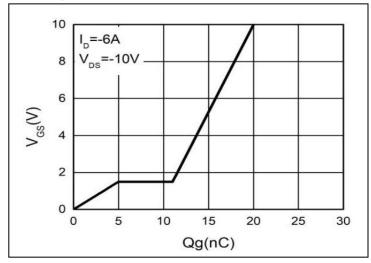


Figure 6. R<sub>DS(ON)</sub> vs Junction Temperature





# **Typical Electrical and Thermal Characteristics**



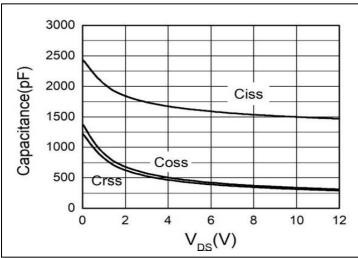
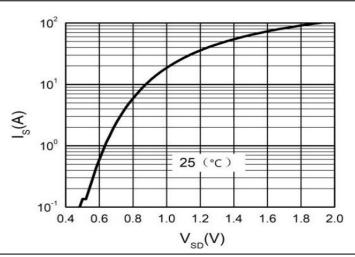
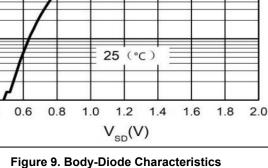


Figure 7. Gate Charge

Figure 8. Capacitance





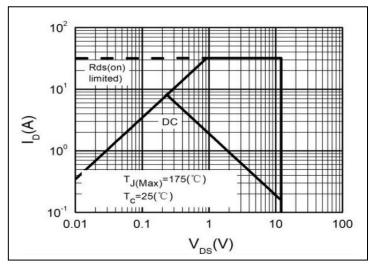
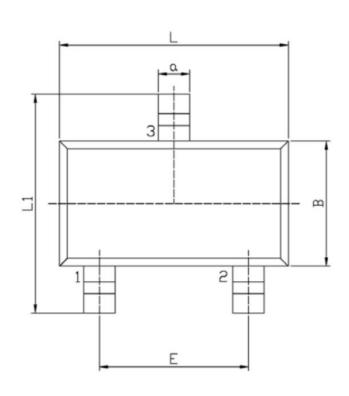
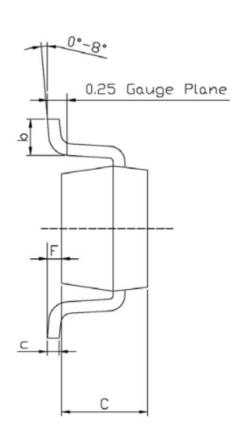


Figure 10. Maximum Safe Operating Area



## **Mechanical Data:**





Unit: mm

Symbol	Dimensions In Millimeters			Dimensions In Millimeters	
	Min	Max	Symbol	Min	Max
L	2.82	3.02	a	0.35	0.50
В	1.50	1.70	С	0.10	0.20
С	0.90	1.30	b	0.35	0.55
L1	2.60	3.00	F	0	0.15
Е	1.80	2.00			





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