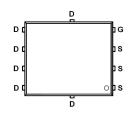
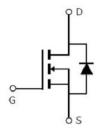


Main Product Characteristics:

V _{DSS}	150V		
R _{DS} (on)	11mΩ (typ.)		
I _D	60A		







PDFN5x6-8L

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	60	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	39	Α
I _{DM}	Pulsed Drain Current ②	240	
P _D @T _C = 25°C	Power Dissipation ③	104	W
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-to-Source Voltage	± 20	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	1.2	°C/W

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	150	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	11	14.3	mΩ	V _{GS} =10V,I _D = 30A
V _{GS(th)}	Gate threshold voltage	2.2	_	3.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	1	μA	V _{DS} = 150V,V _{GS} = 0V
	Cata ta Sauraa famuard la akaga	_	_	100	nA	V _{GS} =20V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	IIA	V _{GS} = -20V
C _{iss}	Input capacitance	_	1352	_		V _{GS} = 0V
Coss	Output capacitance	_	1905	_	pF	V _{DS} = 25V
C _{rss}	Reverse transfer capacitance	_	44	_		f = 1MHz
Qg	Total gate charge	_	36	_		I _D = 20A,
Q _{gs}	Gate-to-Source charge	_	13	_	nC	V _{DS} =75V,
Q _{gd}	Gate-to-Drain("Miller") charge	_	5	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	15	_		
tr	Rise time	_	14	_		V _{GS} =10V, V _{DS} =75V,
t _{d(off)}	Turn-Off delay time	_	40	_	ns	$R_{GEN}=6\Omega$, $I_D=20A$
t _f	Fall time	_	12	_		

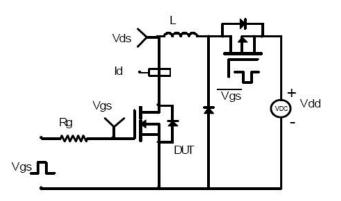
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
1.	Continuous Source Current			60	_	MOSFET symbol
Is	(Body Diode)	<u> </u>	60 A	showing the		
	Pulsed Source Current			240	_	integral reverse
Ism	(Body Diode)	_ _ 2	240	A	p-n junction diode.	
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =30A, V _{GS} =0V
trr	Reverse Recovery Time	_	75	_	ns	$T_J = 25^{\circ}C$, $I_F = 20A$, $di/dt =$
Qrr	Reverse Recovery Charge	_	290	_	nC	100A/µs

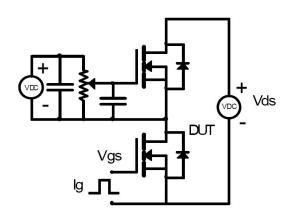


Test Circuits and Waveforms

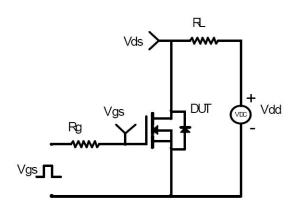
EAS Test Circuit:



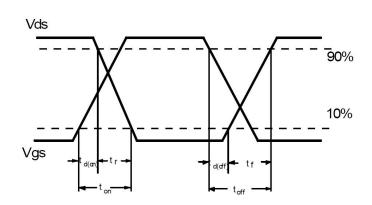
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



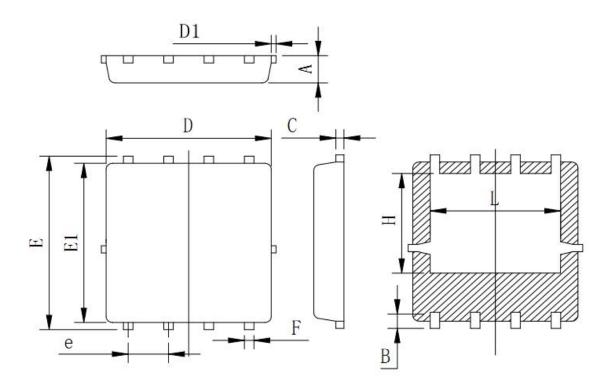
Version: Preliminary

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- $\ \ \,$ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.



Mechanical Data:



Symbol	Min	Тур	Max
Α	0.90	0.95	1.00
В	0.48	0.58	0.68
С	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
Е	5.90	6.05	6.20
El	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
Н	3.27	3.47	3.67
L	3.80	4.00	4.20





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