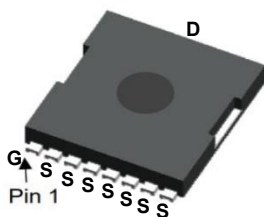
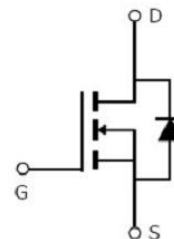


Main Product Characteristics:

V_{DSS}	60V
$R_{DS(on)}$	1.3m Ω (typ.)
I_D	360A


TOLL

Schematic Diagram
Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature


Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ①	360	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ①	216	
I_{DM}	Pulsed Drain Current ②	1440	
$P_D @ T_C = 25^\circ\text{C}$	Power Dissipation ③	138	W
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy @ $L=0.5\text{mH}$	784	mJ
$T_J \quad T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

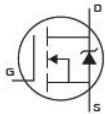
Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
R _{θJC}	Junction-to-case ③	—	0.9	°C/W
R _{θJA}	Junction-to-ambient (t ≤ 10s) ④	—	60	°C/W

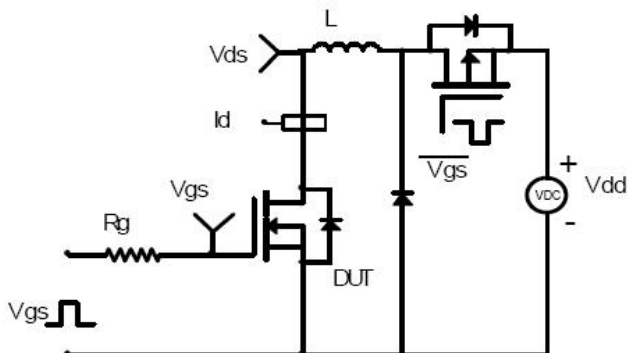
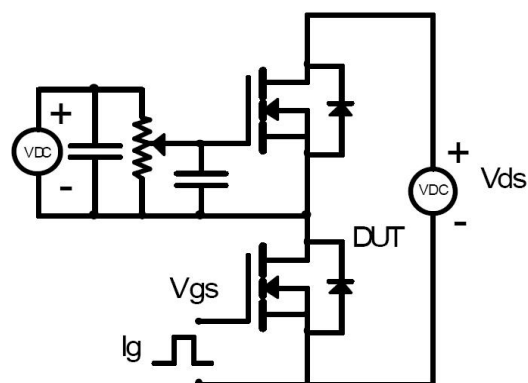
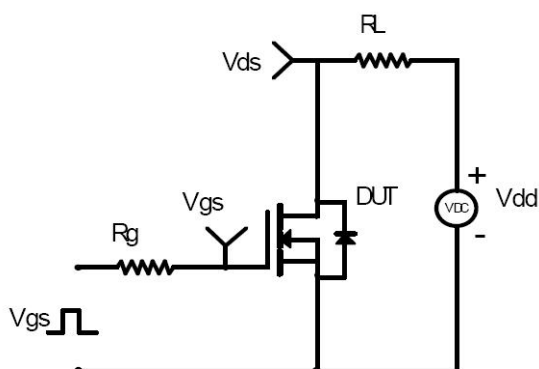
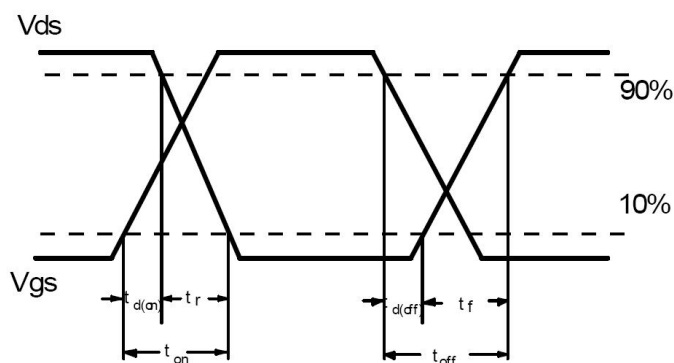
Electrical Characterizes @T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	60	—	—	V	V _{GS} = 0V, I _D = 250μA
R _{DS(on)}	Static Drain-to-Source on-resistance	—	1.3	1.7	mΩ	V _{GS} =10V, I _D = 30A
V _{GS(th)}	Gate threshold voltage	2	—	4	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source leakage current	—	—	1	μA	V _{DS} = 60V, V _{GS} = 0V
I _{GSS}	Gate-to-Source forward leakage	—	—	100	nA	V _{GS} = 20V
		—	—	-100		V _{GS} = -20V
C _{iss}	Input capacitance	—	7395	—	pF	V _{GS} = 0V
C _{oss}	Output capacitance	—	3880	—		V _{DS} = 25V
C _{rss}	Reverse transfer capacitance	—	200	—		f = 1MHz
Q _g	Total gate charge	—	120	—	nC	I _D = 30A, V _{DS} =30V, V _{GS} = 10V
Q _{gs}	Gate-to-Source charge	—	35	—		
Q _{gd}	Gate-to-Drain("Miller") charge	—	30	—		
t _{d(on)}	Turn-on delay time	—	25	—	ns	V _{GS} =10V, V _{DD} =30V, R _{GEN} =3Ω, I _D =30A
t _r	Rise time	—	32	—		
t _{d(off)}	Turn-Off delay time	—	50	—		
t _f	Fall time	—	25	—		

Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	360	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode)	—	—	1440	A	
V _{SD}	Diode Forward Voltage	—	—	1.2	V	I _S =30A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	—	70	—	ns	T _J = 25°C, I _F =30A, di/dt =
Q _{rr}	Reverse Recovery Charge	—	120	—	nC	100A/μs

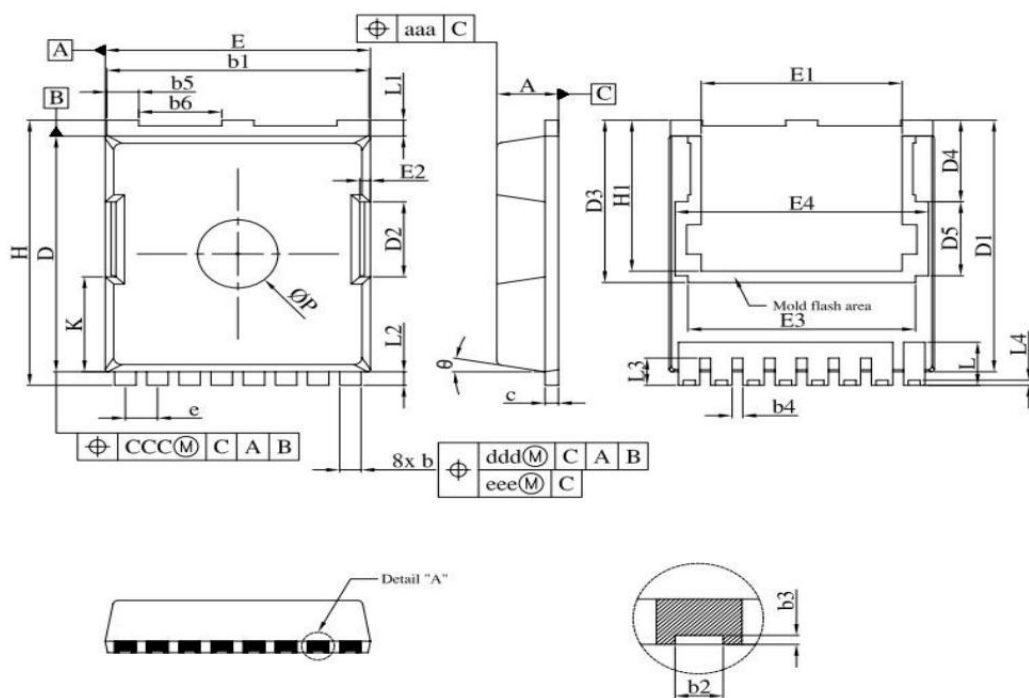
Test Circuits and Waveforms

EAS Test Circuit:

Gate Charge Test Circuit:

Switching Time Test Circuit:

Switching Waveforms:


Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ④ The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$

Mechanical Data:



SYMBOL	COMMON		
	MILLIMETER		
	MIN.	NOMINAL	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.45	0.55
b3	0.05	0.100	/
b4	0.30	0.40	0.50
b5	1.10	1.20	1.30
b6	3.00	3.10	3.20
c	0.40	0.50	0.60
D	10.28	10.38	10.55
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D3	7.15		
D4	3.59		
D5	3.26		
e	1.10	1.20	1.30
E	9.80	9.90	10.00
E1	7.40	7.50	7.60
E2	0.30	0.40	0.50
E3	8.50		
E4	9.46		
H	11.50	11.68	11.85
H1	6.55	6.65	6.75
K	4.08	4.18	4.28
L	1.60	1.90	2.10
L1	0.50	0.70	0.90
L2	0.50	0.60	0.70
L3	1.00	1.20	1.30
L4	0.13	0.23	0.33
P	2.85	3.00	3.15
θ	10° REF		
aaa	0.20		
ccc	0.20		
ddd	0.25		
eee	0.20		

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