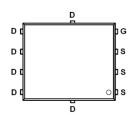
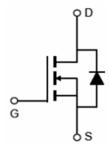


Main Product Characteristics:

V _{DSS}	150V
R _{DS} (on)	9mΩ(typ.)
I _D	80A







PDFN 5x6-8L

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latestprocessing techniquesto achieve the high cell density and reduces the on-resistancewith high repetitiveavalanche rating. These features combine to makethis design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

AbsoluteMax Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V①	80	^
I _{DM}	Pulsed Drain Current②	240	A
P _D @T _C = 25°C	Power Dissipation③	160	W
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-to-Source Voltage	± 20	V
Eas	Single Pulse Avalanche Energy @ L=0.3mH	80	mJ
T _J T _{STG}	Operating Junction and Storage TemperatureRange	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
$R_{ heta JC}$	Junction-to-case③	1	0.78	°C/W

Electrical Characterizes@T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	150	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
R _{DS(on)}	Static Drain-to-Source on-resistance	_	9	10	mΩ	V _{GS} =10V,I _D =20A	
V _{GS(th)}	Gate threshold voltage	3	_	4.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
I _{DSS}	Drain-to-Source leakage current	_	_	1	μΑ	V _{DS} =135V,V _{GS} = 0V	
	Coto to Course forward looked	_	_	100	- Λ	V _{GS} =20V	
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -20V	
Q_g	Total gate charge	_	70	_		I _D = 44A,	
Q_{gs}	Gate-to-Source charge	_	27	_	nC	V _{DS} =75V,	
Q_{gd}	Gate-to-Drain("Miller") charge	_	18	_		V _{GS} = 10V	
t _{d(on)}	Turn-on delay time	_	21	_		\/ 40\/ \/ 75\/	
t _r	Rise time	_	21	_		V _{GS} =10V, V _{DS} =75V,	
t _{d(off)}	Turn-Off delay time	_	36	_	ns	$R_{GEN}=2\Omega$	
t _f	Fall time	_	9	_		I _D = 44A	
C _{iss}	Input capacitance	_	5130	_		$V_{GS} = 0V$	
Coss	Output capacitance	_	1670	_	pF	V _{DS} =25V	
C _{rss}	Reverse transfercapacitance	_	175	_		f = 100kHz	

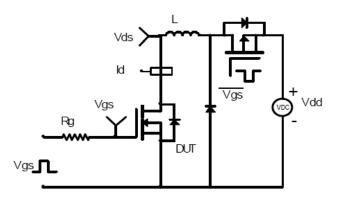
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			80	^	MOSFET symbol
Is	(Body Diode)	_	_	80	A	showing the
1	Pulsed Source Current			240	۸	integral reverse
ISM	(Body Diode)	_		240	A	p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	1.3	V	I _S =20A, V _{GS} =0V
trr	Reverse Recovery Time	_	75	_	ns	1 1 di/dt 100 \(\frac{1}{100} \)
Qrr	Reverse Recovery Charge	_	285	_	nC	I _F =I _S ,di/dt=100A/us

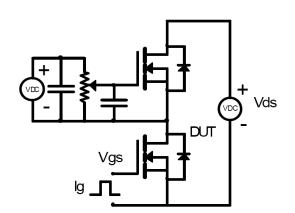


Test Circuits and Waveforms

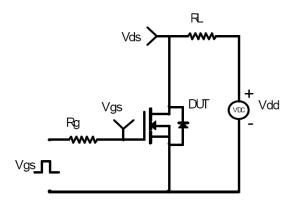
EAS Test Circuit:



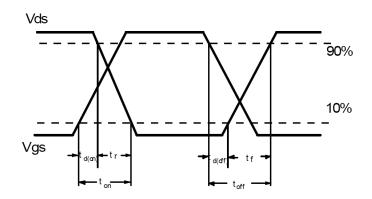
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:

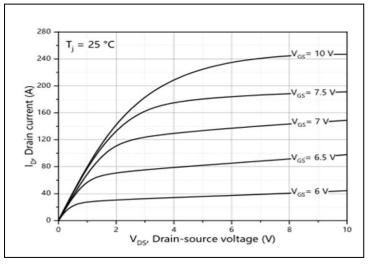


Notes:

- ①Calculated continuous current based on maximum allowablejunction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.



Typical Electrical and Thermal Characteristics



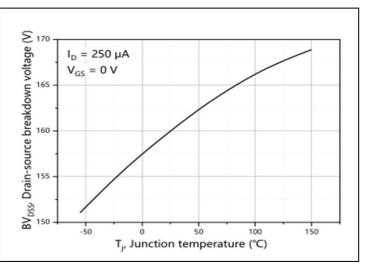
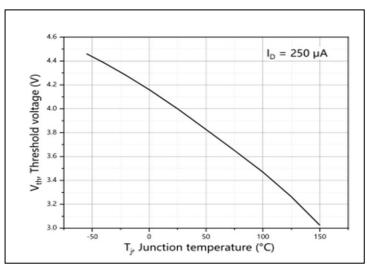


Figure1.Typical Output Characteristics

Figure 2. Drain-to-Source Breakdown Voltage vs. Temperature



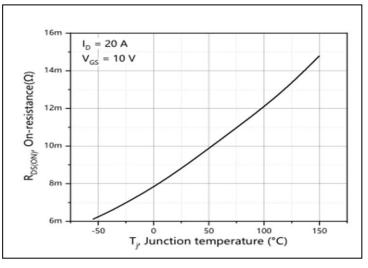
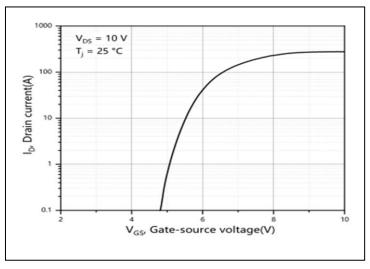


Figure3.Gate to Source Cut-off Voltage

Figure 4. Normalized On-Resistance vs. Junction Temperature



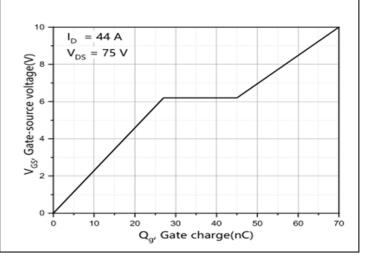


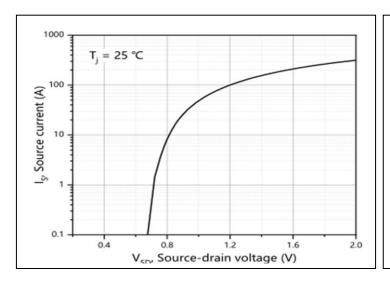
Figure5.Typical Transfer Characteristics

Figure 6. Typ. Gate Charge





Typical Electrical and Thermal Characteristics



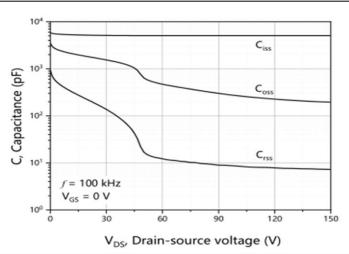
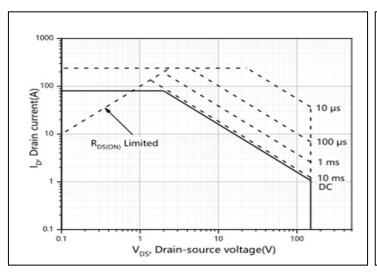


Figure7.Forward Characteristics of Body Diode

Figure8.Capacitance



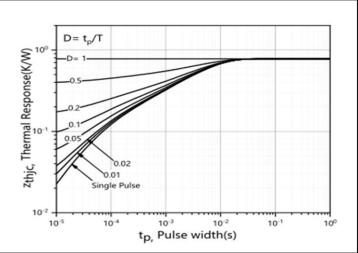


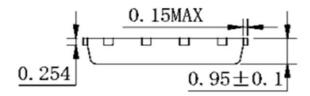
Figure 9. Maximum Safe Operating Area

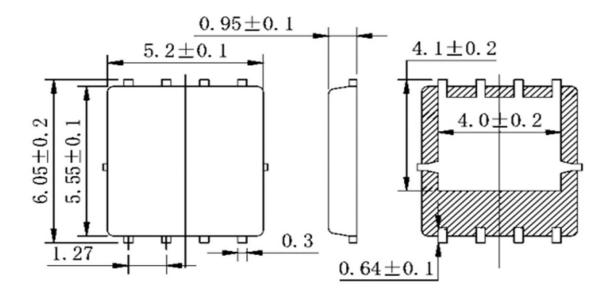
Figure 10. Normalized Maximum Transient Thermal



Mechanical Data:

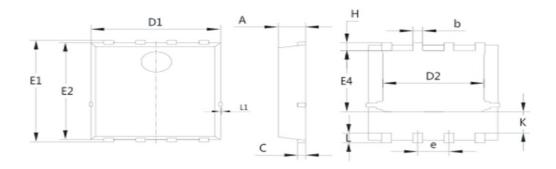
Option1







Option2



Symbol	mm					
	Min	Nom	Max			
Α	1.00	1.10	1.20			
b	0.30	0.40	0.50			
С	0.154	0.254	0.354			
D1	5.00	5.20	5.40			
D2	3.80	4.10	4.25			
е	1.17	1.27	1.37			
E1	5.95	6.15	6.35			
E2	5.66	5.86	6.06			
E4	3.52	3.72	3.92			
н	0.40	0.50	0.60			
L	0.30	0.60	0.70			
L1	0.12 REF					
K	1.15	1.30	1.45			





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