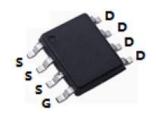
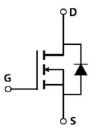


## **Main Product Characteristics**

V <sub>DSS</sub>	100V				
R <sub>DS</sub> (on)	17mΩ (typ.)				
I <sub>D</sub>	<b>8A</b> ①				





SOP-8

Schematic Diagram

## **Features and Benefits**

- Low R<sub>DS(on)</sub> & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and reverse body recovery
- 150°C operating temperature



## **Applications**

- Consumer electronic power supply
- Motor control
- Synchronous-rectification
- Isolated DC/DC convertor

# **Description**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

# **Absolute Max Rating**

Symbol Parameter		Max.	Units	
I <sub>D</sub> @ TC = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V ①	8	Δ.	
I <sub>DM</sub>	Pulsed Drain Current ②	32	A A	
P <sub>D</sub> @TC = 25°C	Power Dissipation ③	3.5	W	
V <sub>DS</sub>	Drain-Source Voltage	100	V	
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V	
E <sub>AS</sub>	Single Pulse Avalanche Energy @ L=0.5mH	42	mJ	
I <sub>AS</sub>	Avalanche Current @ L=0.5mH		Α	
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range		°C	



# **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
Reja	Junction-to-ambient (t $\leq 10s$ ) $\textcircled{4}$	_	35.7	°C/W

# **Electrical Characterizes** @T<sub>A</sub>=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	100	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA
Г	Static Ducin to Course on activities	_	16	20	mΩ	V <sub>GS</sub> =10V,I <sub>D</sub> =10A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	_	26		V <sub>GS</sub> =4.5V,I <sub>D</sub> =7A
V <sub>GS(th)</sub>	Gate threshold voltage	1	_	2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
I <sub>DSS</sub>	Drain-to-Source leakage current	_	_	1	μA	V <sub>DS</sub> =100V,V <sub>GS</sub> = 0V
0-4-4-6	Cata to Source forward lookage	_	_	100	nA	V <sub>GS</sub> =20V
I <sub>GSS</sub>	Gate-to-Source forward leakage	_	_	-100		V <sub>GS</sub> = -20V
Qg	Total gate charge	_	19	_		I <sub>D</sub> = 5A,
Q <sub>gs</sub>	Gate-to-Source charge	_	2.5	_	nC	V <sub>DS</sub> =50V,
Q <sub>gd</sub>	Gate-to-Drain("Miller") charge	_	5	_		V <sub>GS</sub> = 10V
t <sub>d(on)</sub>	Turn-on delay time	_	8.1	_		$V_{GS}=10V, V_{DD}=50V,$ $R_{GEN}=3\Omega$
tr	Rise time	_	11.7	_		
t <sub>d(off)</sub>	Turn-Off delay time	_	23.3	_	ns	
t <sub>f</sub>	Fall time	_	5.7	_		I <sub>D</sub> =10A
C <sub>iss</sub>	Input capacitance	_	927	_		V <sub>GS</sub> = 0V
Coss	Output capacitance	_	108	_	pF	V <sub>DS</sub> = 50V
C <sub>rss</sub>	Reverse transfer capacitance	_	2.5	_		f = 1MHz

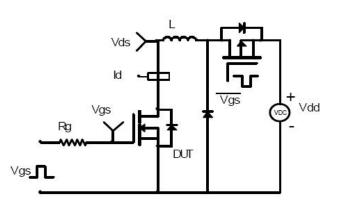
# **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode) ①	_	_	8	А	MOSFET symbol showing the
	Pulsed Source Current					showing the integral reverse
I <sub>SM</sub>	(Body Diode)	_	_	32	А	p-n junction diode.
$V_{\text{SD}}$	Diode Forward Voltage	_	_	1.3	V	I <sub>S</sub> =20A, V <sub>GS</sub> =0V, T <sub>J</sub> = 25°C
t <sub>rr</sub>	Reverse Recovery Time	_	50	_	ns	$T_J = 25^{\circ}C$ , $I_F = 5A$ , $di/dt =$
Q <sub>rr</sub>	Reverse Recovery Charge	_	65	_	nC	100A/μs

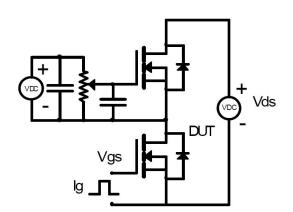


## **Test Circuits and Waveforms**

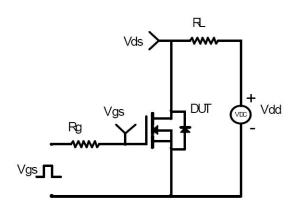
#### **EAS Test Circuit:**



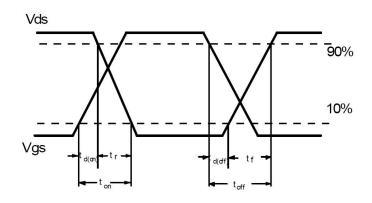
#### **Gate Charge Test Circuit:**



#### **Switching Time Test Circuit:**



### **Switching Waveforms:**



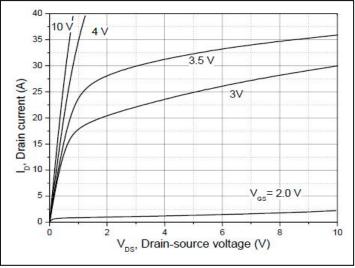
### Notes:

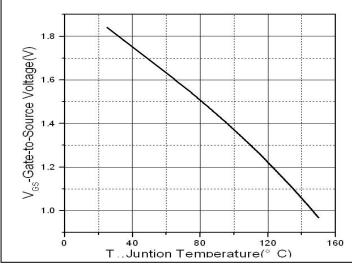
- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of  $R_{\texttt{9JA}}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C





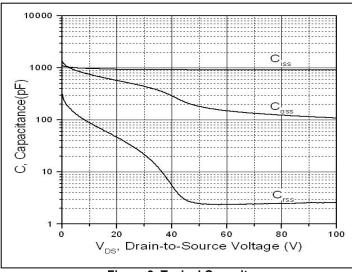
# **Typical Electrical and Thermal Characteristics**





**Figure 1. Typical Output Characteristics** 





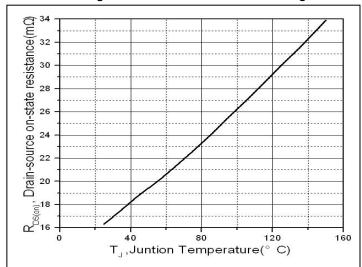
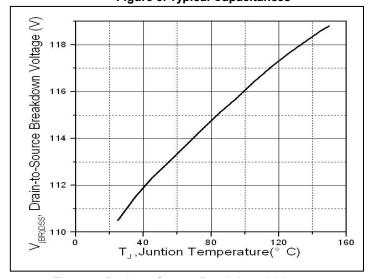


Figure 3. Typical Capacitances

Figure 4. Drain-to-Source On-state Resistance



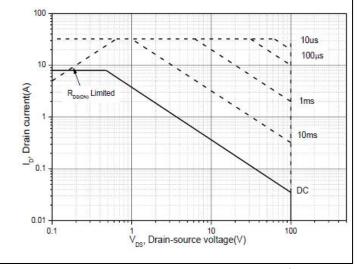
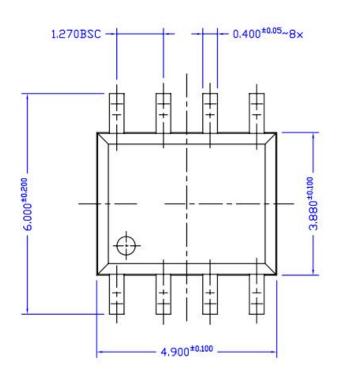


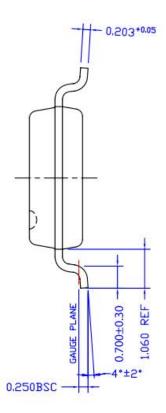
Figure 5. Drain-to-Source Breakdown Voltage

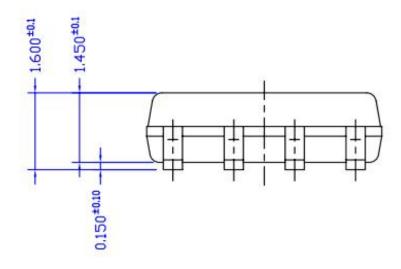
Figure 6. Safe Operation Area(Tc=25℃)



# **Mechanical Data:**











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