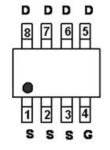
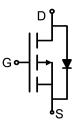


Main Product Characteristics:

V _{DSS}	-40V					
R _{DS} (on)	11.6mΩ (typ.)					
I _D	-13A _①					







SOP-8

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current ①	-13		
I _{DM}	Pulsed Drain Current ②	-52	Α	
P _D @T _C = 25°C	Power Dissipation ③	4	W	
V _{DS}	Drain-Source Voltage	-40	V	
V _{GS}	Gate-to-Source Voltage	± 20	V	
Eas	Single Pulse Avalanche Energy @ L=0.5mH	128	mJ	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{0JA}	Junction-to-ambient (t ≤ 10s) ④	_	31.2	°C/W

Electrical Characterizes @T_A=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-40	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	11.6	15	mΩ	V _{GS} =-10V,I _D = -10A
		_	15	22	mΩ	V _{GS} =-4.5V,I _D =-8A
V _{GS(th)}	Gate threshold voltage	-1	_	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	-1	μA	V _{DS} = -40V,V _{GS} = 0V
1	Cata to Source forward lookage	_	_	100		V _{GS} = 20V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -20V
Qg	Total gate charge	_	20	_		I _D = -10A,
Q _{gs}	Gate-to-Source charge	_	8	_	nC	V _{DS} =-32V,
Q_{gd}	Gate-to-Drain("Miller") charge	_	8.5	_		V _{GS} = -4.5V
t _{d(on)}	Turn-on delay time	_	21.8	_		
t _r	Rise time	_	87.2	_		V _{GS} =-10V, V _{DS} =-20V,
t _{d(off)}	Turn-Off delay time	_	49.5	_	ns	$R_{GEN}=3\Omega,I_D=-20A$
t _f	Fall time	_	54.8	_		
C _{iss}	Input capacitance	_	3860	_		V _{GS} = 0V
Coss	Output capacitance	_	250	_	pF	V _{DS} = -25V
C _{rss}	Reverse transfer capacitance	_	240	_		f = 1MHz

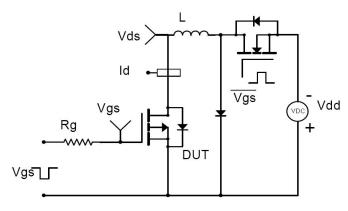
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current	_	_	-13	А	MOSFET symbol
	(Body Diode) ①					showing the
I _{SM}	Pulsed Source Current	_	_	-52	А	integral reverse G → + + + + + + + + + + + + + + + + + +
	(Body Diode) ①					p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	-1.2	V	I _S =-1A, V _{GS} =0V

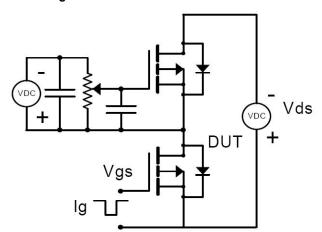


Test Circuits and Waveforms

EAS Test Circuit:

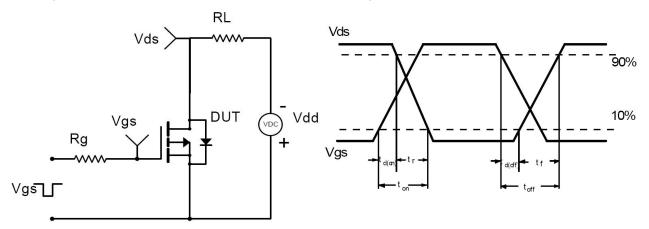


Gate Charge Test Circuit:



Switching Time Test Circuit:

Switching Waveforms:

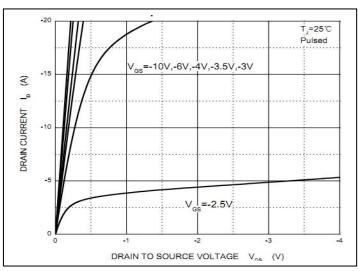


Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C



Typical Electrical and Thermal Characteristics



-1.5 V_{GS}-Gate-to-Source Voltage(V) -1.3 T_J,Juntion Temperature(° C)

Figure 1. Typical Output Characteristics

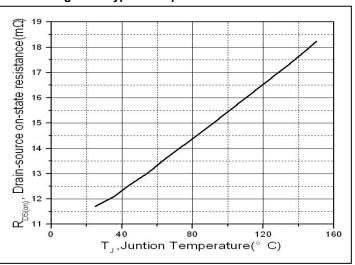


Figure 2. Normalized V_{GS}(th) vs. Junction Temperature

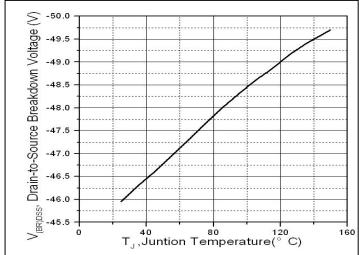


Figure 3. Normalized On-Resistance vs. Junction Temperature

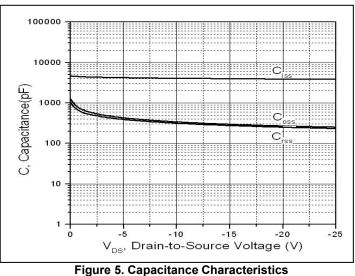


Figure 4. Drain-to-Source Breakdown Voltage vs. Junction

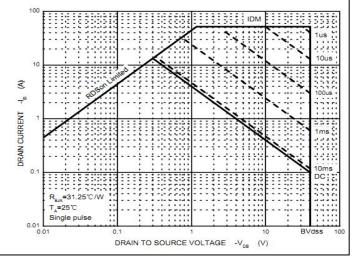
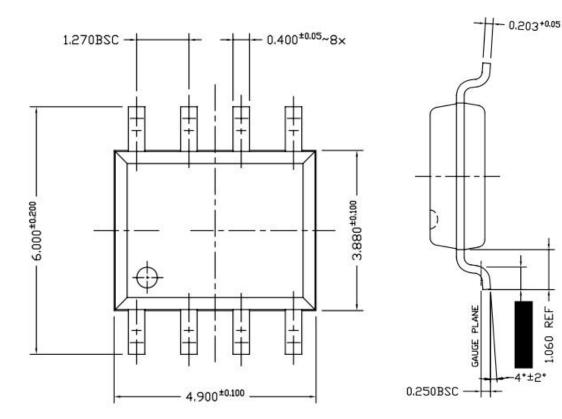


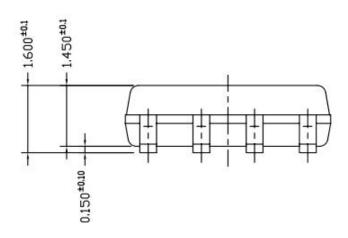
Figure 6. Safe Operation Area



Mechanical Data:

SOP-8 Package Outline (Unit:mm)







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