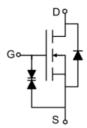


## **Main Product Characteristics:**

$V_{DSS}$	20V				
R <sub>DS</sub> (on)	18mΩ(typ.)				
I <sub>D</sub>	6.5A				







SOT-23

Marking and Pin Assignments

Schematic Diagram

## **Features and Benefits**

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



# **Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

# **Absolute Max Rating:**

Symbol	Parameter	Max.	Units
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V①	6.5	
I <sub>DM</sub>	Pulsed Drain Current②	30	A
P <sub>D</sub> @T <sub>C</sub> = 25°C	Power Dissipation③	1.4	W
V <sub>DS</sub>	Drain-Source Voltage	20	V
V <sub>GS</sub>	Gate-to-Source Voltage	± 8	V
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C



## **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient④		90	°C/W

# **Electrical Characterizes** $@T_A=25$ $^{\circ}$ Cunless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Ъ	Static Drain-to-Source on-resistance	_	18	22	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6.5A	
$R_{DS(on)}$		_	24	30	mΩ	V <sub>GS</sub> =2.5V, I <sub>D</sub> =5.5A	
V <sub>GS(th)</sub>	Gate threshold voltage	0.4	_	1	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
I <sub>DSS</sub>	Drain-to-Source leakage current	_	_	1	μA	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V	
1	0.1.0	_	_	±1	uA	V <sub>GS</sub> =±4.5V,V <sub>DS</sub> =0V	
$I_{GSS}$	Gate-to-Source forward leakage	_	_	±10	uA	V <sub>GS</sub> =±8V,V <sub>DS</sub> =0V	
Qg	Total gate charge	_	10	_		V <sub>DS</sub> =10V I <sub>D</sub> =6.5A	
$Q_{gs}$	Gate-to-Source charge	_	2.3	_	nC		
$Q_{gd}$	Gate-to-Drain("Miller") charge	_	3	_		V <sub>GS</sub> =4.5V	
t <sub>d(on)</sub>	Turn-on delay time	_	6.5	_		V <sub>DD</sub> =10V I <sub>D</sub> =1A V <sub>GS</sub> =5V	
t <sub>r</sub>	Rise time	_	13	_			
$t_{\text{d(off)}}$	Turn-Off delay time	_	50	_	ns		
tf	Fall time	_	30	_		R <sub>GEN</sub> =3Ω	
C <sub>iss</sub>	Input capacitance	_	1160	_		V <sub>GS</sub> = 0V	
Coss	Output capacitance	_	200	_	pF	V <sub>DS</sub> = 10V	
C <sub>rss</sub>	Reverse transfer capacitance	_	140	_		f =1MHz	

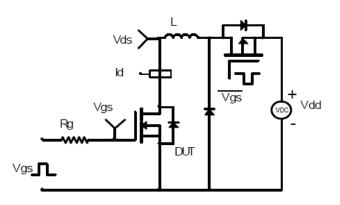
# **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			6.5	А	MOSFET symbol
	(Body Diode)	_				showing the
I <sub>SM</sub>	Pulsed Source Current	_	_	30	А	integral reverse
	(Body Diode)					p-n junction diode.
V <sub>SD</sub>	Diode Forward Voltage	_	0.76	1	V	V <sub>GS</sub> =0V,I <sub>S</sub> =1A

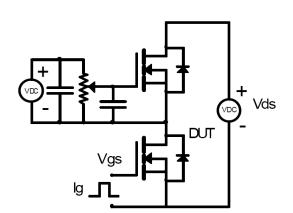


## **Test Circuits and Waveforms**

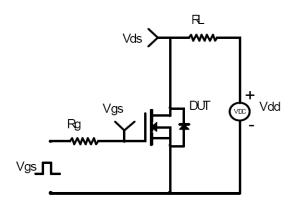
#### **EAS Test Circuit:**



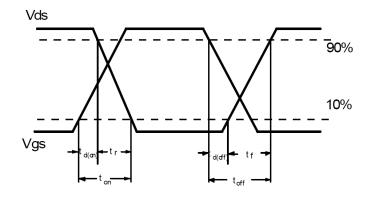
### **Gate Charge Test Circuit:**



### **Switching Time Test Circuit:**



#### **Switching Waveforms:**



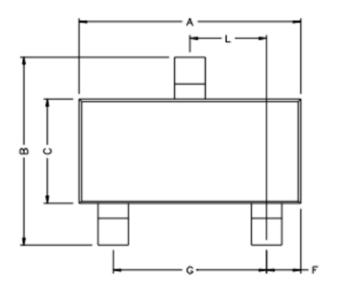
### Notes:

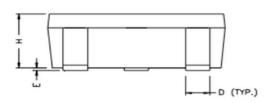
- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of R $_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25  $^{\circ}$ C

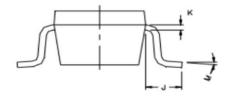


## **Mechanical Data:**

## SOT-23 Package Outline(Unit:mm)







REF.	Millimeter		REF.	Millimete	
KEF.	Min.	Max.	KEF.	Min.	Max.
Α	2.80	3.00	G	1.80	2.00
В	2.30	2.50	Н	0.90	1.1
С	1.20	1.40	K	0.10	0.20
D	0.30	0.50	J	0.35	0.70
E	0	0.10	L	0.92	0.98
F	0.45	0.55	М	0°	10°





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