

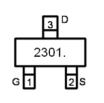
SSF2301UP

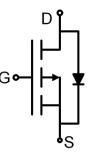
Main Product Characteristics:

V _{DSS}	-20V				
R _{DS} (on)	59mΩ(typ.)				
I _D	-2.6A				



SOT-23





Marking and Pin Assignments

Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V①	-2.6	А
I _{DM}	Pulsed Drain Current2	-10	A
$P_{D} @ T_{C} = 25^{\circ}C$	Power Dissipation ③	1.25	W
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-to-Source Voltage	± 12	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{θJC}	Junction-to-case③		100	°C/W

Electrical Characterizes @T_A=25°Cunless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-20	_	—	V	$V_{GS} = 0V, I_D = -250 \mu A$	
R _{DS(on)}	Static Drain-to-Source on-resistance	—	59	75	mΩ	V _{GS} =-4.5V,I _D =-2A	
			76	95	mΩ	V _{GS} =-2.5V,I _D =-1.8A	
$V_{GS(th)}$	Gate threshold voltage	-0.4	—	-1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
I _{DSS}	Drain-to-Source leakage current	—	—	-1	μA	V_{DS} =-20V, V_{GS} = 0V	
	Cata to Source forward lookage	_	_	100	nA	V _{GS} =12V	
I _{GSS}	Gate-to-Source forward leakage	_	—	-100	na I	V _{GS} = -12V	
Qg	Total gate charge	_	5.8	—		I _D = -2.3A,	
Q_{gs}	Gate-to-Source charge	_	0.84	—	nC	V _{DS} =-6V,	
Q_{gd}	Gate-to-Drain("Miller") charge	_	1.6	—		$V_{GS} = -4.5V$	
t _{d(on)}	Turn-on delay time	_	7	—			
t _r	Rise time	_	14	—		V_{GS} =-4.5V, V_{DD} =-20V,	
t _{d(off)}	Turn-Off delay time	_	20	—	ns	$R_{GEN}=3\Omega$	
t _f	Fall time	_	7	—		R _L =10Ω	
C _{iss}	Input capacitance	_	394	—		$V_{GS} = 0V$	
Coss	Output capacitance	_	48	—	pF	V _{DS} = -20V	
C _{rss}	Reverse transfer capacitance	—	41	_		f = 1MHz	

Source-Drain Ratings and Characteristics

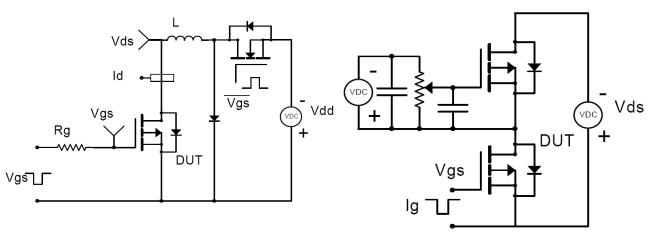
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
Is	Continuous Source Current (Body Diode)	_	_	-2.6	А	MOSFET symbol	
I _{SM}	Pulsed Source Current (Body Diode)	_	_	-10	A	integral reverse	
V _{SD}	Diode Forward Voltage		-0.8	-1.2	V	I _S =-1A, V _{GS} =0V	

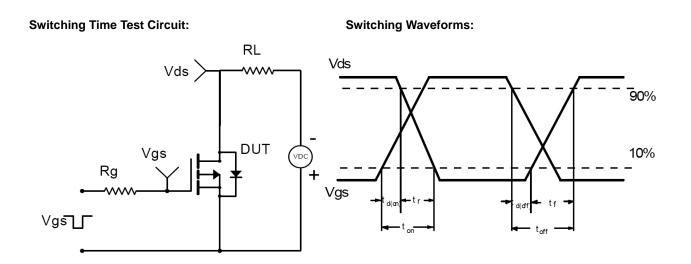


Test Circuits and Waveforms



Gate Charge Test Circuit:





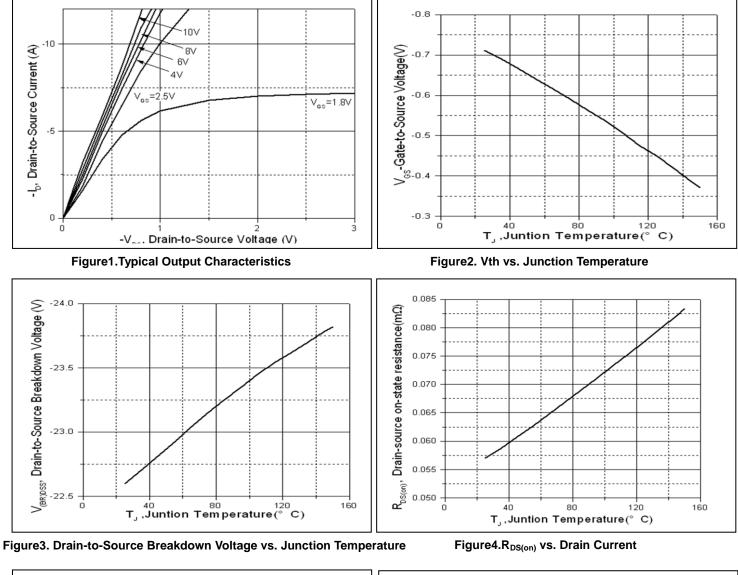
Notes:

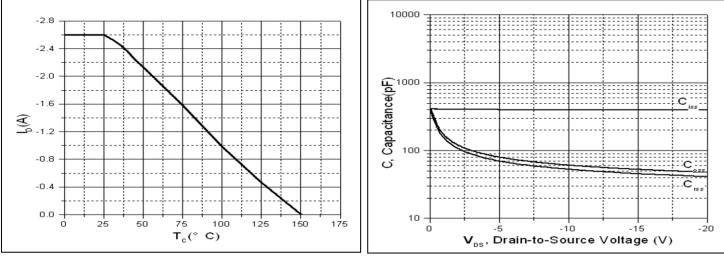
- ①Calculated continuous current based on maximum allowable junction temperature.
- 2 Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

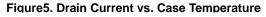


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Typical Electrical and Thermal Characteristics







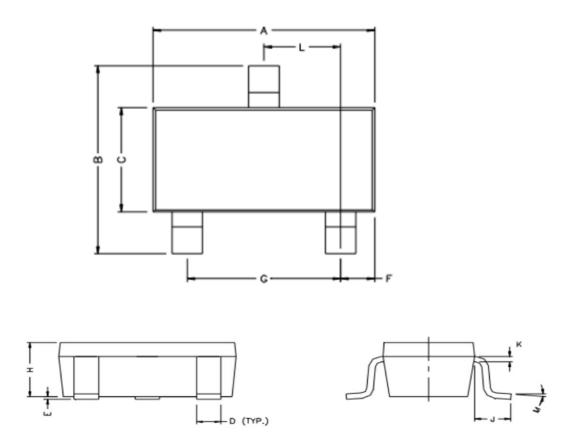




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Mechanical Data:

SOT-23 Package Outline(Unit:mm)



REF.	Millimeter		REF.	Millimete		
NEF.	Min.	Max.	KEF.	Min.	Max.	
Α	2.80	3.00	G	1.80	2.00	
B	2.30	2.50	Н	0.90	1.1	
С	1.20	1.40	K	0.10	0.20	
D	0.30	0.50	J	0.35	0.70	
E	0	0.10	L	0.92	0.98	
F	0.45	0.55	М	0°	10°	



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