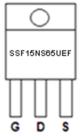
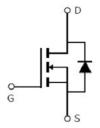


Main Product Characteristics:

V _{DSS}	650V		
R _{DS} (on)	0.23Ω (typ.)		
I _D	15A		







TO-220F

Marking and Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute max Rating:

Symbol	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V ①	15	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V ①	9.5	Α
I _{DM}	Pulsed Drain Current ②	45	
P _D @T _C = 25°C	Power Dissipation ③	32	W
V _{DS}	Drain-Source Voltage	650	V
V _{GS}	± 30	V	
E _{AS}	Single Pulse Avalanche Energy @ L=20mH	382	mJ
I _{AS}	Avalanche Current @ L=20mH	6.2	Α
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C



Thermal Resistance

Symbol	Characterizes		Max.	Units
R _{0JC} Junction-to-case ③		_	3.9	°C/W
$R_{\theta JA}$	Junction-to-ambient (t \leq 10s) (4)	_	62.5	°C/W

Electrical Characterizes $@T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	650	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	_	0.23	0.29	Ω	V _{GS} =10V,I _D = 7.5A
$V_{GS(th)}$	Gate threshold voltage	2.7	_	3.7	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I _{DSS}	Drain-to-Source leakage current	_	_	1	μA	$V_{DS} = 650 V, V_{GS} = 0 V$
1	Onto to Course forward lanks as	_	_	100	nA	V _{GS} =30V
I_{GSS}	Gate-to-Source forward leakage	_	_	-100	IIA	V _{GS} = -30V
C _{iss}	Input capacitance	_	1184	_		$V_{GS} = 0V$
Coss	Output capacitance	_	47	_	pF	V _{DS} = 100V
C _{rss}	Reverse transfer capacitance	_	1.65	_		f = 100kHz
Q_g	Total gate charge	_	20	_		$I_D = 8A$,
Q_{gs}	Gate-to-Source charge	_	5	_	nC	V _{DS} =400V,
Q_{gd}	Gate-to-Drain("Miller") charge	_	7	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	25	_		
t _r	Rise time		39	_	nS	V _{GS} =10V, V _{DD} =300V,
t _{d(off)}	Turn-Off delay time	_	65	_	113	$R_{GEN}=10\Omega, I_D=15A$
t _f	Fall time	_	38	_		

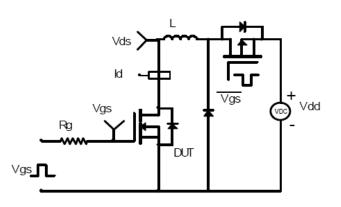
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current		_	15	А	MOSFET symbol
	(Body Diode)	_				showing the
I _{SM}	Pulsed Source Current	_	_	45	А	integral reverse
	(Body Diode)					p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	1.3	V	I _S =15A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	260	_	nS	$T_J = 25$ °C, $I_F = 8A$, $di/dt =$
Q _{rr}	Reverse Recovery Charge	_	3	_	uC	100A/µs

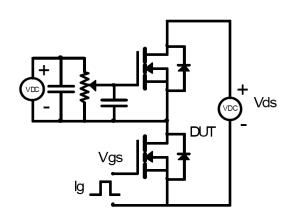


Test Circuits and Waveforms

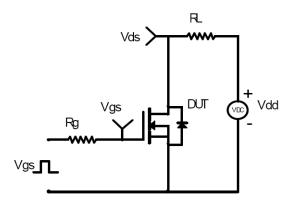
EAS Test Circuit:



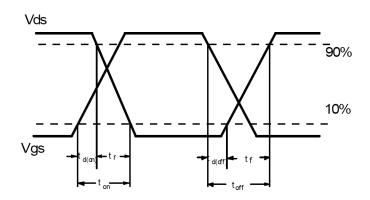
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



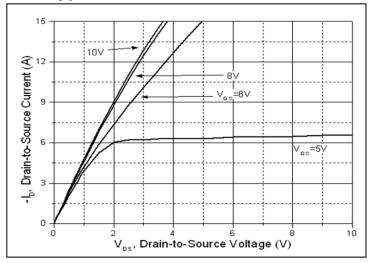
Notes:

- ①The maximum current rating is limited by bond-wires.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4 The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C





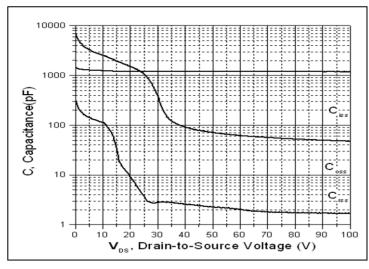
Typical Electrical and Thermal Characteristics



100 V_{OS}=20 V T_j = 25 °C 0.1 0.1 0.01 0 2 4 6 8 10 V_{GS}, Gate-source voltage(V)

Figure 1. Typical Output Characteristics

Figure 2. Typical Transfer Characteristics



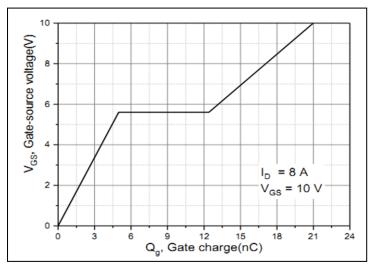
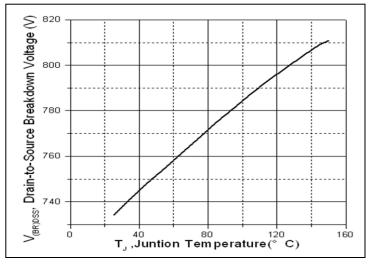


Figure 3. Typical Capacitance

Figure 4. Typical Gate Charge



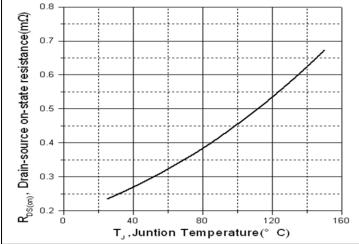


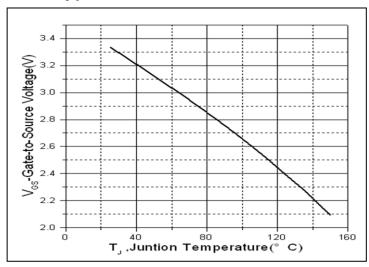
Figure 5. Drain-to-Source Breakdown Voltage vs. Temperature

Figure 6. Normalized On-Resistance vs. Temperature





Typical electrical and thermal characteristics



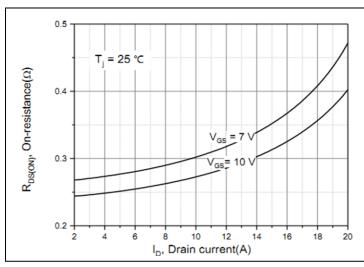
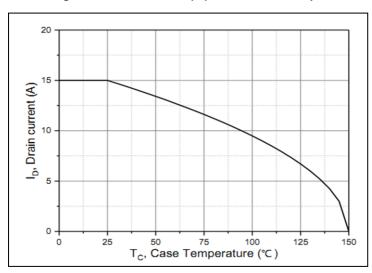


Figure 7. Normalized V_{GS}(th) vs. Junction Temperature

Figure 8. Drain-to-Source On-state Resistance



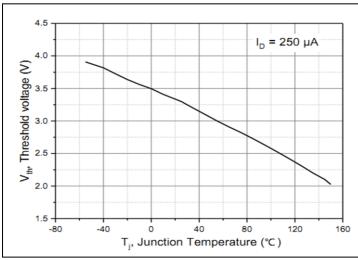
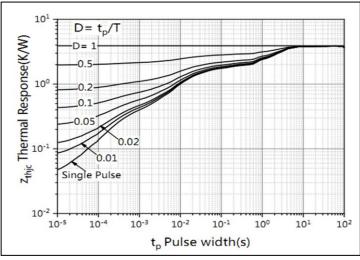


Figure 9. Drain Current

Figure 10. Threshold Voltage



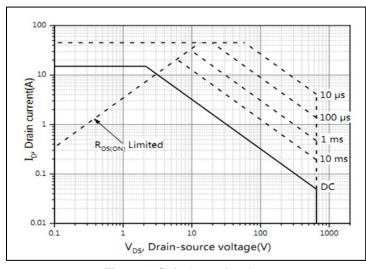
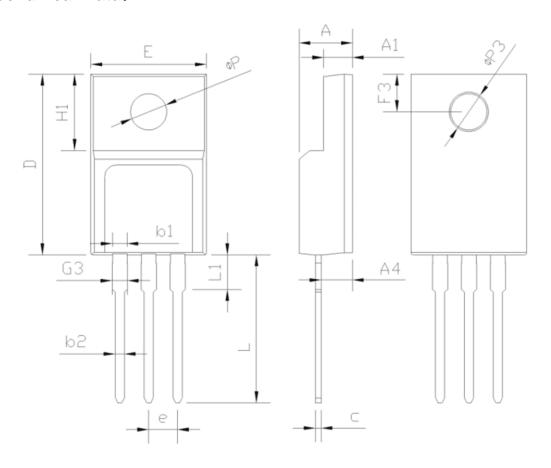


Figure 11. Transient Thermal Impedance

Figure 12. Safe Operation Area



Mechanical Data:



SYMBOL	mm					
STWBOL	MIN	NOM	MAX			
E	9.96	10.16	10.36			
Α	4.50	4.70	4.90			
A1	2.34	2.54	2.74			
A4	2.56	2.76	2.96			
С	0.40	0.50	0.65			
D	15.57	15.57 15.87 16.17				
H1	6.70REF					
е		2.54BSC				
L	12.68	12.98	13.28			
L1	2.88	2.88 3.03				
ФР	3.03	3.03 3.18 3				
ФР3	3.15	3.45	3.65			
F3	3.15	3.15 3.30 3.45				
G3	1.25	1.25 1.35 1.55				
b1	1.18	1.18 1.28 1.43				
b2	0.70	0.70 0.80 0.95				





ATTENTION:

- Any and all Silikron products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Silikron representative nearest you before using any Silikron products described or contained herein in such applications.
- Silikron assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Silikron products described or contained herein.
- Specifications of any and all Silikron products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- Silikron Microelectronics (Suzhou) Co.,Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all Silikron products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Silikron Microelectronics (Suzhou) Co.,Ltd.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Silikron believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Silikron product that you intend to use.